

TITLE

DES-815 Rev.A3 Circuit Diagram

DES-815 Rev. A3

生效日期: 87.6.26

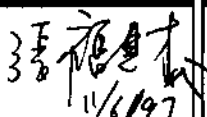
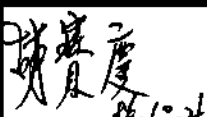
Circuit Diagram

It contains only one part of component boards.

- I. Bottom Board 23 Pages
- II. LED Display Board (see DES-815 Rev. A2)

Note: LED Board Refer to NPRDES815----A2-01

REVISION	STATUS
PAGE	REV
0 ~ 0-1	A
1 ~ 24	A

APPROVED	CHECKED	DEPARTMENT	REVIEWED
 11/6/97	 86.10.24	ED5	萬其永

文件履歷表

DOC NO. 305DES815 - - - A3 -

VERSION **A**

PAGE 01 0-1 / 24

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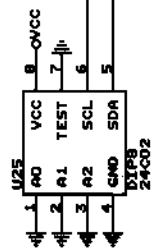
[illegible]

D-Link

er Ko
11/96

1

VCC
C161
0.1u

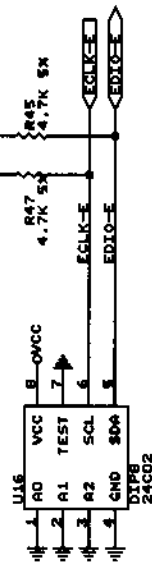


Connect to TI-SWITCH

High Speed Signal:

- (1) ECLK
- (2) ECLK-E
- (3) EDIO
- (4) EDIO-E

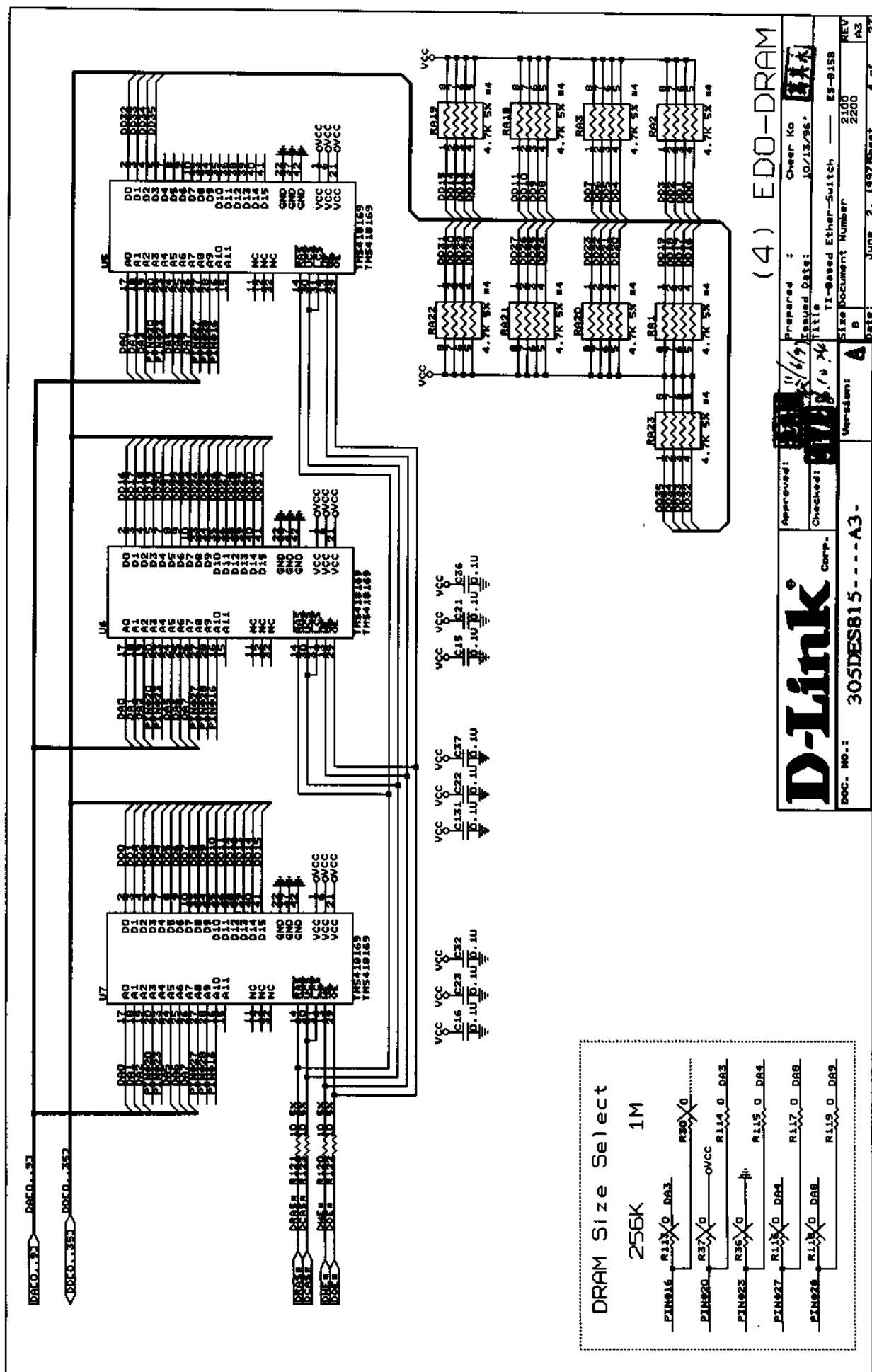
VCC
C52
0.1u



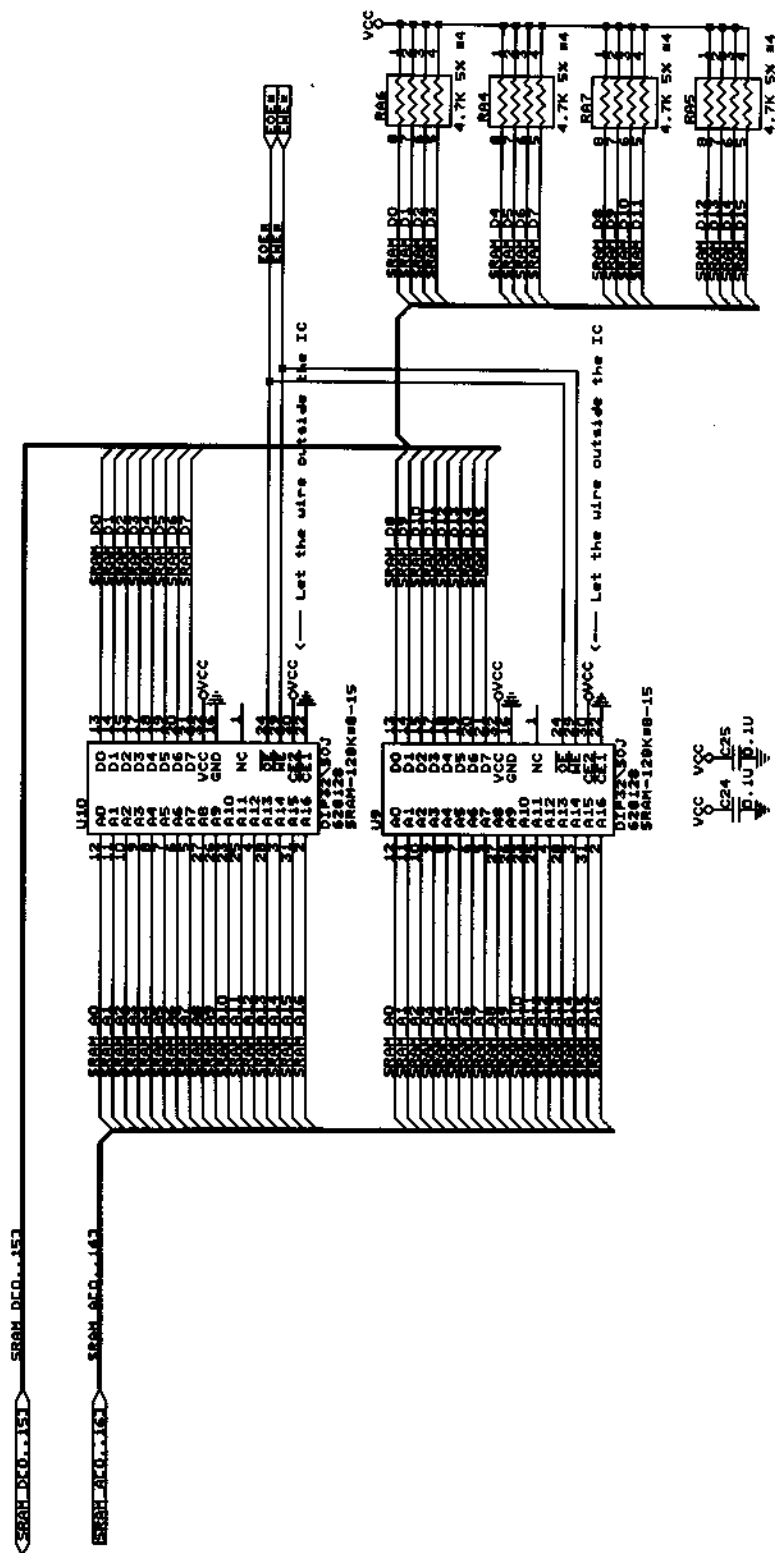
Connect to TI-EALE

(3) 2 Wire S-EEPROM

D-Link Corp.		Approved: 10/16/97	Prepared: 10/16/97	Cheer Ko
DOC. NO.: 305DES815		Checked: 10/16/97	Issued Date: 10/13/96	REV
A3		Title: TI-Based Ether-Switch		ES-0158
Revision: A		Size Document Number: 2100		REV
Date: Nov 25, 1997		Sheet 3 of 3		REV



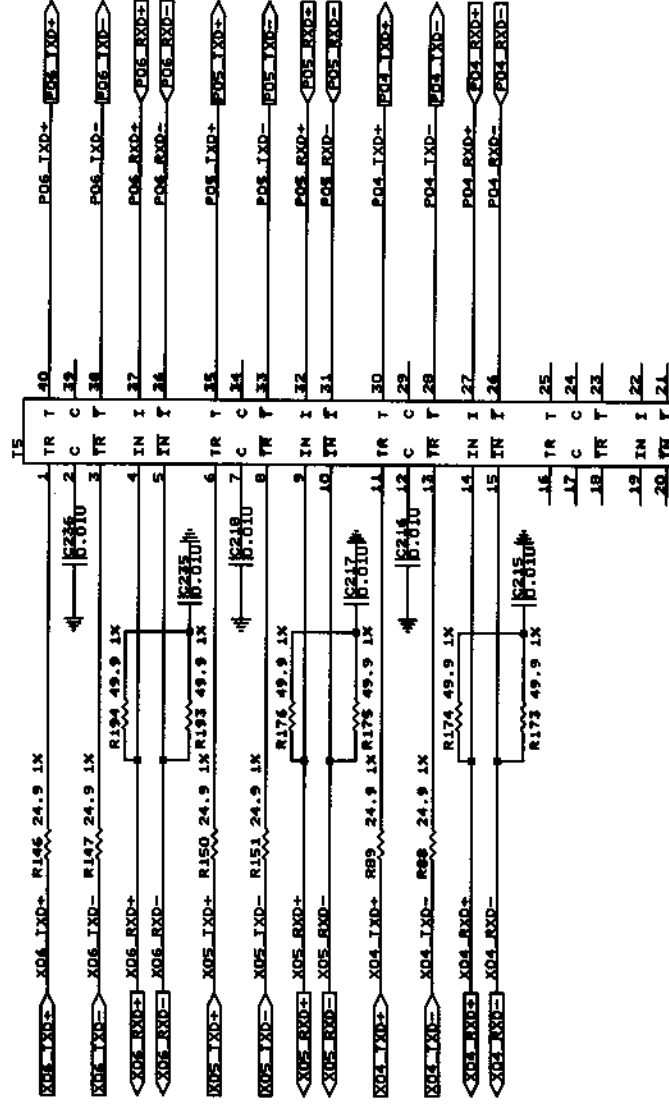
.....SRAM DEC...152..... TEST PIN
SRAM DEC...153..... TEST PIN



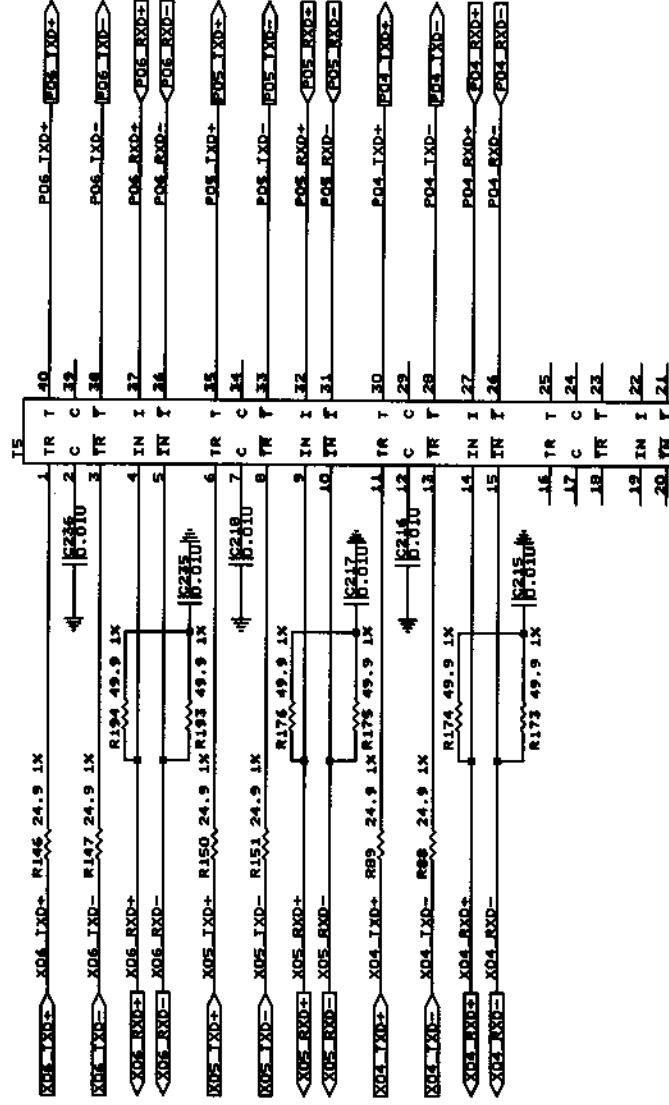
(5) EALE-SRAM

D-Link Corp. DOC. No.: 305DES815-...-A3-	Approved: Date: 11/6/94	Prepared: Date: 10/13/95	Checked: Date: 10/13/95
	Version: A	Title: FI-Based Ether-Switch	Part Number: ES-0156
Date: May 29, 1997		Sheet 5 of 23	

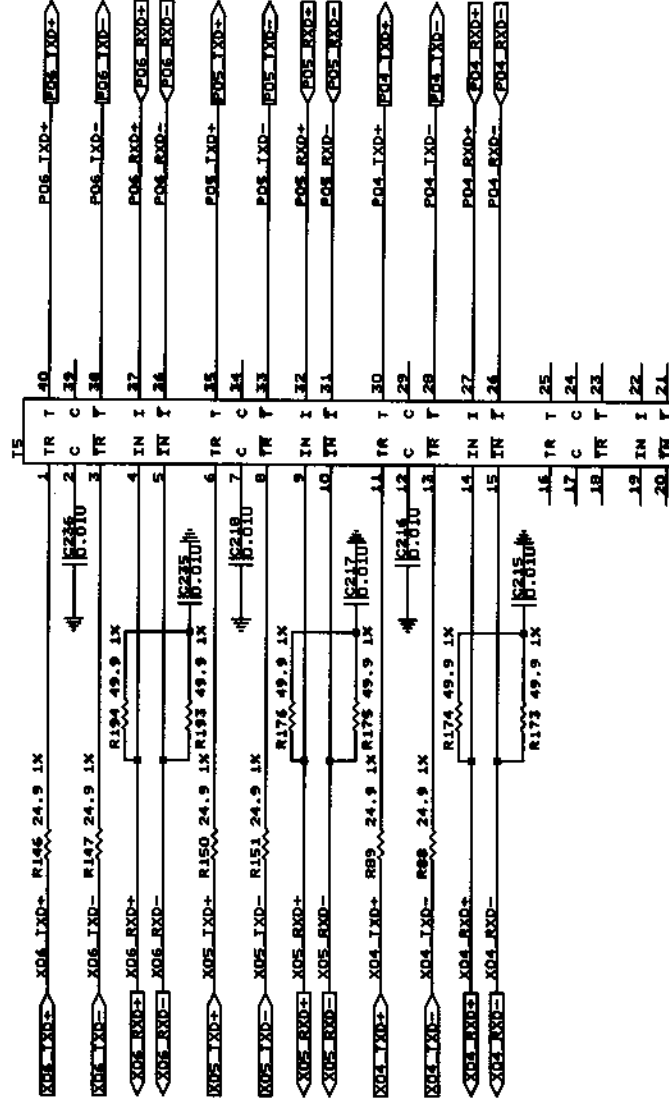
PORT 6



PORT 5



PORT 4



PORT NC

High Speed Signal:

- (1) X04_TXD+
- (2) X04_TXD-
- (3) X04_RXD+
- (4) X04_RXD-

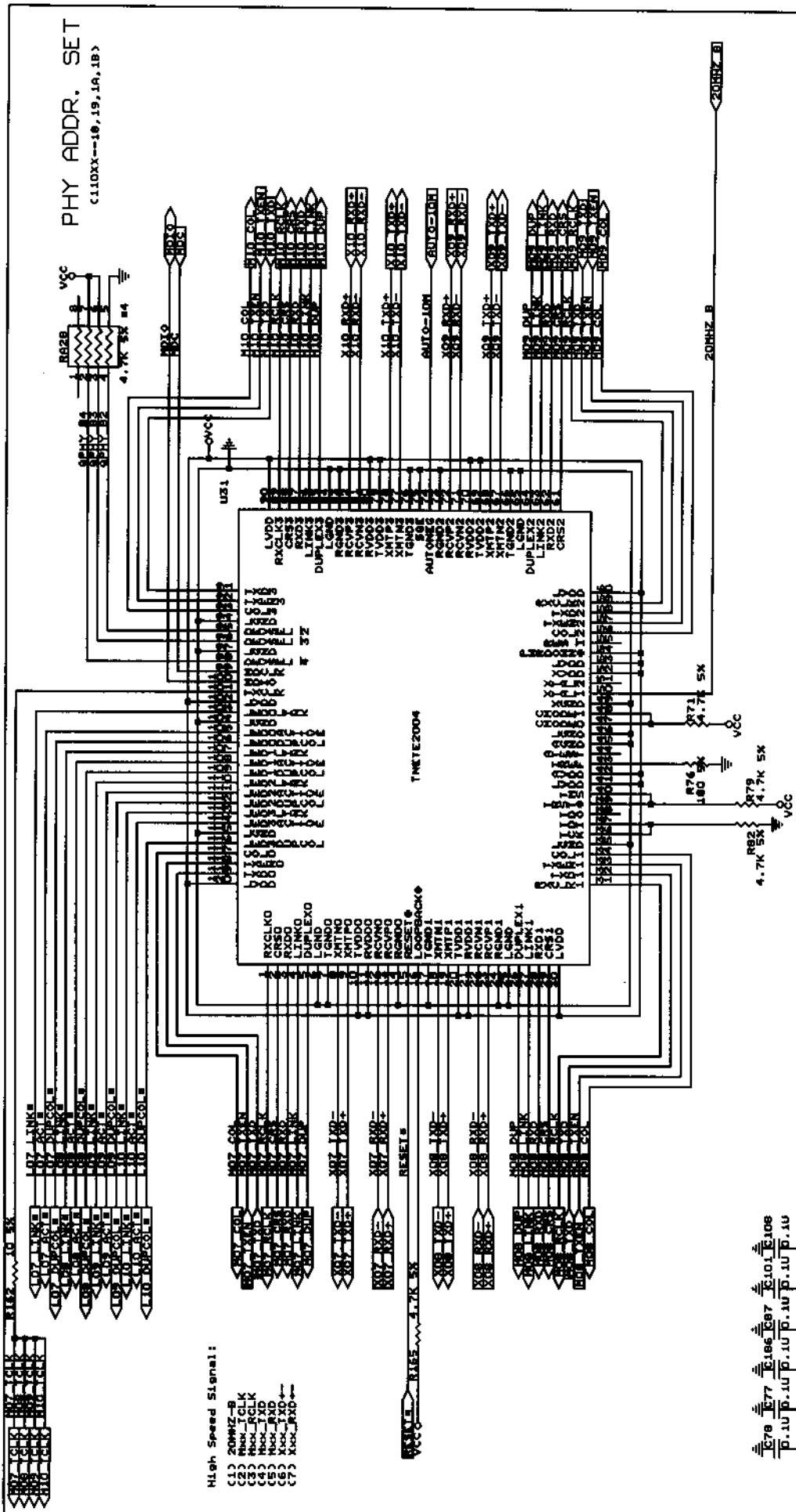
(7) 10Mb ports 4..6: Xformer

D-Link

TI-SWITCH NO.: NC, 4, 5, 6
PHY - ADDRESS: NC, 15, 16, 17

DOC. NO.: 30SDS815-...-A3-
Version: A
Date: Nov 29, 1997 Sheet 7 of 23

Approved: [Signature]
Checked: [Signature]
Prepared: [Signature]
Issued Date: 10/13/96
Title: TI-Based Ether-Switch
Size Document Number: 2100
REV: A3

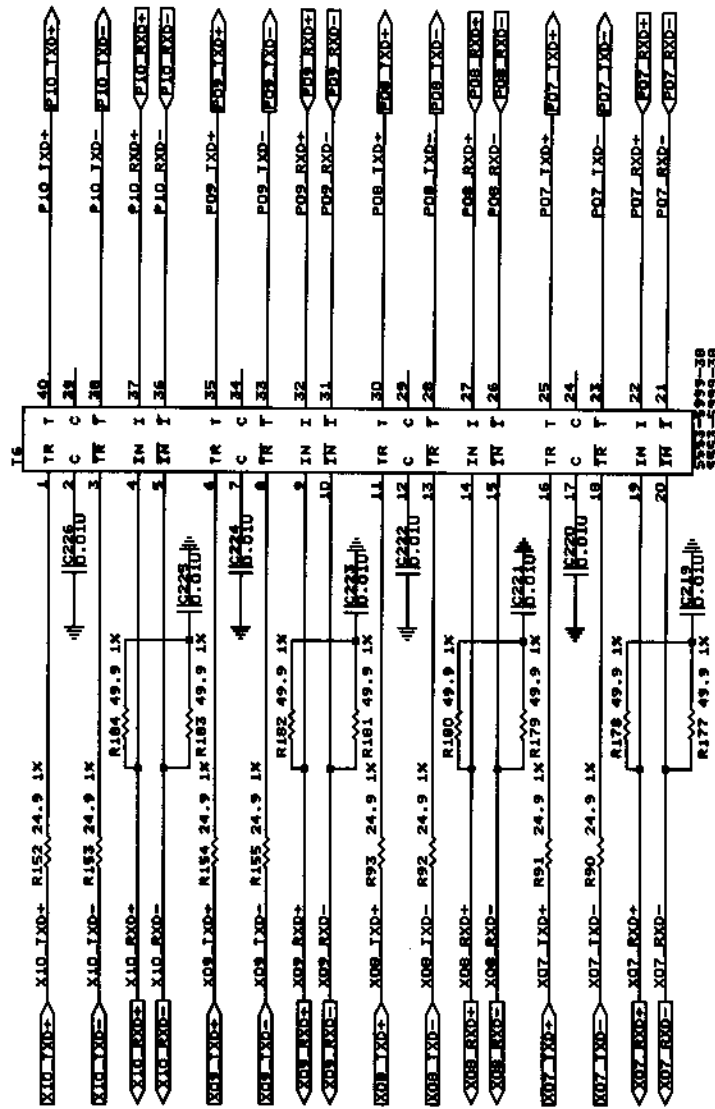


(8) 10Mb ports 7..10: QPH

D-Link Corp.		Approved: 11/6/94	Prepared: 11/6/94	Cheer Ko
DOC. NO.1		Checked: 11/6/94	Issued Date: 10/13/94	
30SDES815- - - A3 -		Title: 10/100/1000 Mbps Ethernet PHY		
		Version: A		
		Size Document Number: 2100		
		REV: A3		
		REV: 2200		
		REV: 1997Sheet		
		REV: 8 of 23		

TI-SWITCH NO.1: 7, 8, 9, 10
PHY - ADDRESS: 10, 19, 1A, 1B

PORT 10



PORT 9

PORT 8

PORT 7

High Speed Signal:

- (1) Xxx_TXD+
- (2) Xxx_TXD-
- (3) Xxx_RXD+
- (4) Xxx_RXD-

(9) 10Mb ports 7..10: Xformer



DOC. NO.: 305DES815-----A3-

Approved: [Signature] Prepared: 11/6/94

Checked: [Signature] Issued Date: 10/13/96

Version: 4

Size: 2100

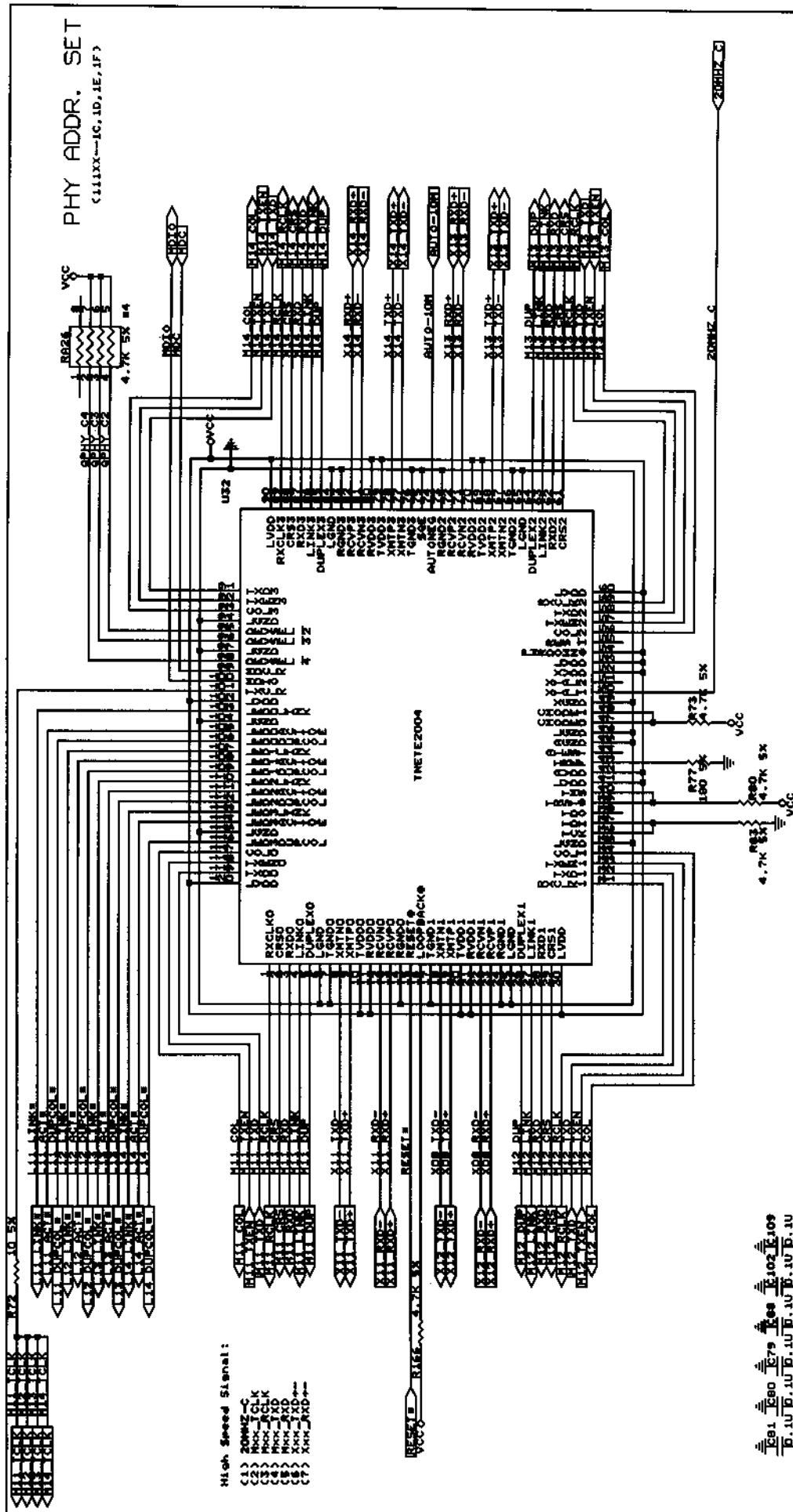
Doc Number: 2200

Rev: A3

Sheet: 9 of 23

TI-SWITCH NO.: 7, 8, 9, 10
PHY - ADDRESS: 16, 19, 1A, 1B

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(10) 10Mb ports 11..14: QPHY

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DOC. NO.: 305DES815-----A3-

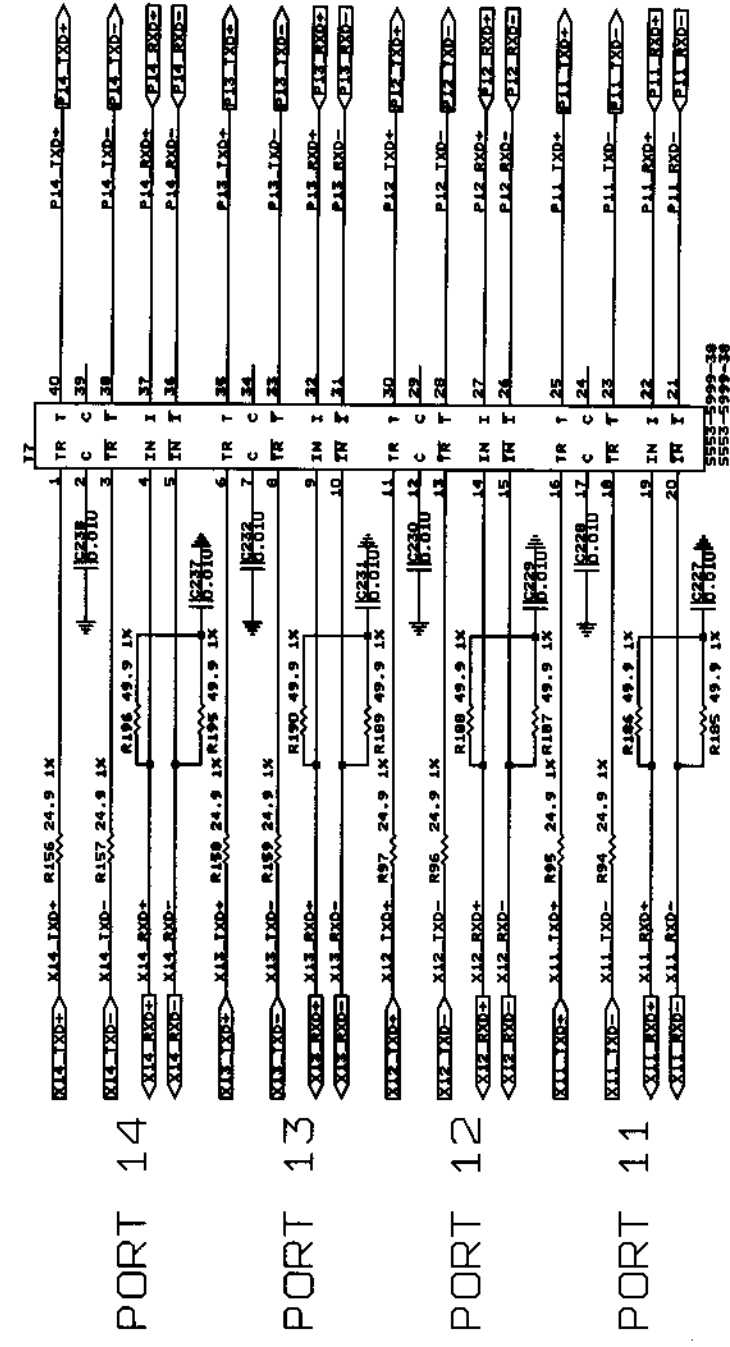
TVI-SWITCH NO.: 11, 12, 13, 14
PHY - ADDRESS: 1C, 1D, 1E, 1F

Version: A	Size Document
11/6/97	Prepared :
11/6/97	Issued Date:
11/6/97	Title
11/6/97	TI-Based

(11) 10Mb ports 11..14: Xformer

High Speed Signal:

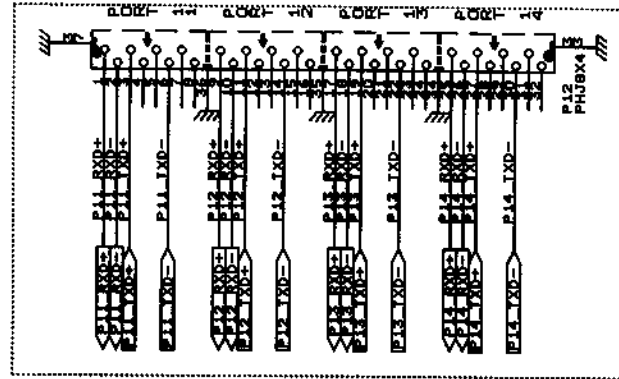
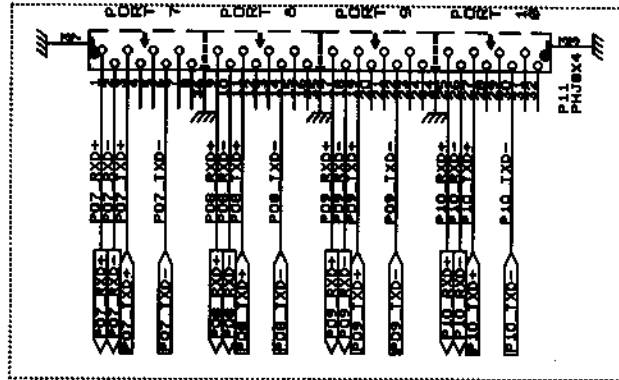
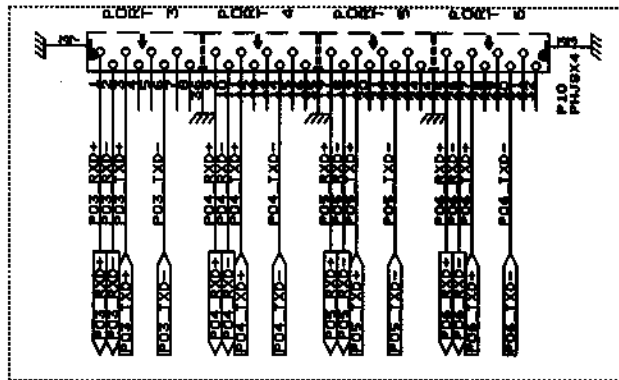
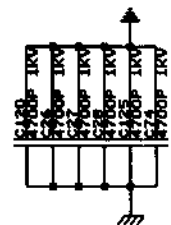
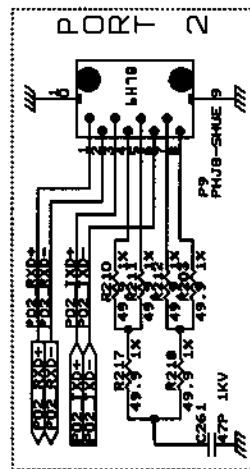
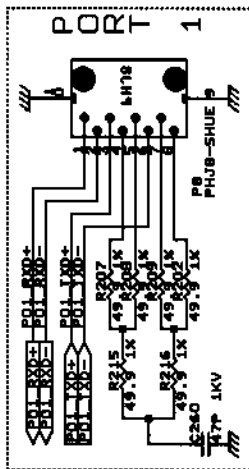
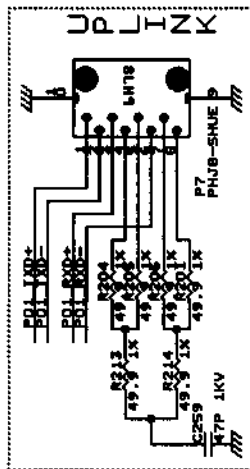
- (1) Xxx_TXD+
- (2) Xxx_TXD-
- (3) Xxx_RXD+
- (4) Xxx_RXD-



TI-SWITCH NO.: 11, 14, 15, 16
PHY - ADDRESS: 1C, 1D, 1E, 1F

Approved: [Signature]
Checked: [Signature]

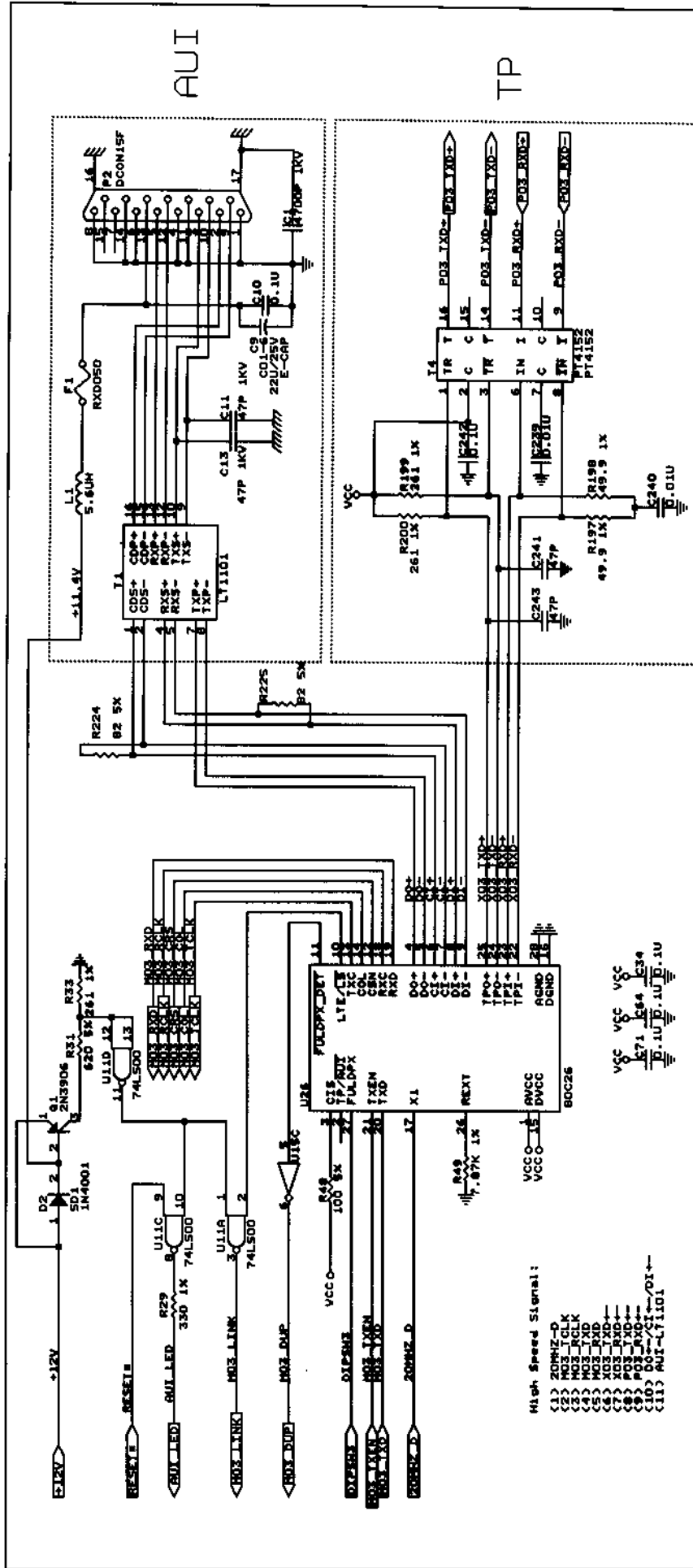
Prepared: 11/14/97
Issued Date: 10/13/96
Title: TI-based Ether-Switch
Size Document Number: 2100
Version: A
Date: May 29, 1997 Sheet 1 of 23



High Speed Signal:
ALL

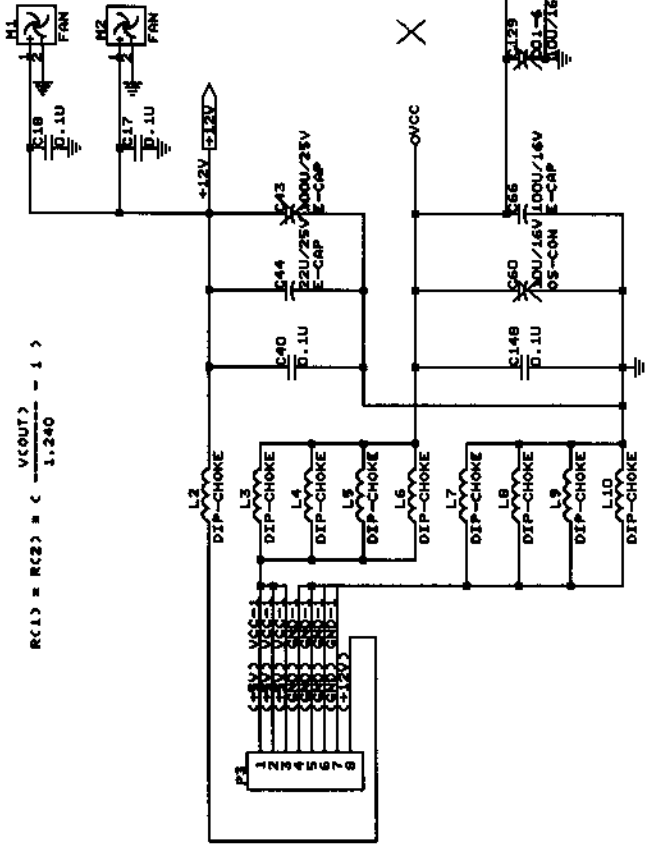
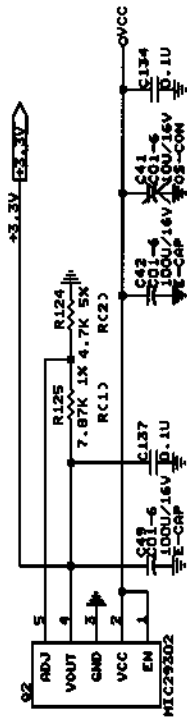
(14) Phone-Jack RJ-45

D-Link Corp. 305DES815---A3-	Approved:	Prepared:	Checked:	Issued Date: 10/13/96	Title: 11-Based Ether-Switch	Version: A	Size: 2100 Sheet: 14 of 23
	Doc. No.: 305DES815---A3-	Checked:	Issued Date: 10/13/96	Title: 11-Based Ether-Switch	Version: A	Size: 2100 Sheet: 14 of 23	Date: June 2, 1997



(15) 10Mbps port 3: AUI/TP

D-Link Corp.		Approved: U697	Prepared: U697	Cheer Ko
DOC. NO.: 305DES815-A3-		Checked: U697	Issued Date: 10/13/96	
		Title: 10Mbps Ether-Switch		
		Size: 2100		
		REV: A3		
		Date: June 2, 1997 Sheet 15 of 23		



$$R(1) = R(2) = C - \frac{V(OUT)}{1.240} - 1$$

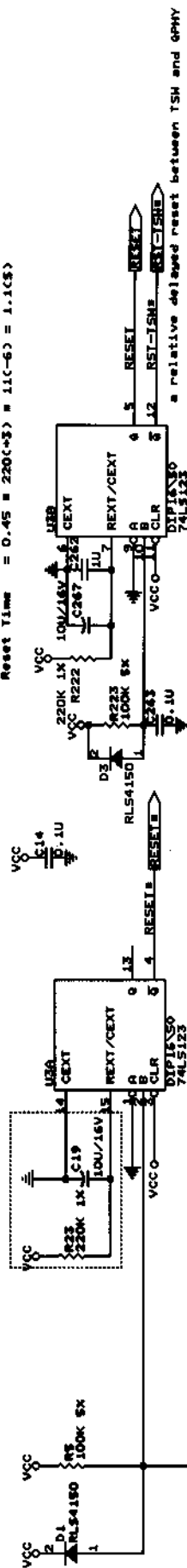
(16) POWER

D-Link Corp.		Approved: 11/6/97	Prepared: 11/6/97	Checked: 11/6/97	Issued Date: 10/13/96	Rev: ES-0158
DOC. NO.: 305DES815-...A3-		Title: 11-Based Ether-Switch		Size: 2100		REV: A3
Version: A		Date: June 2, 1997		Sheet: 16 of 23		

RESET

Reset Time = $0.45 \times 220 \times 10^{-6} \times 10^{-6} = 1(\mu s)$

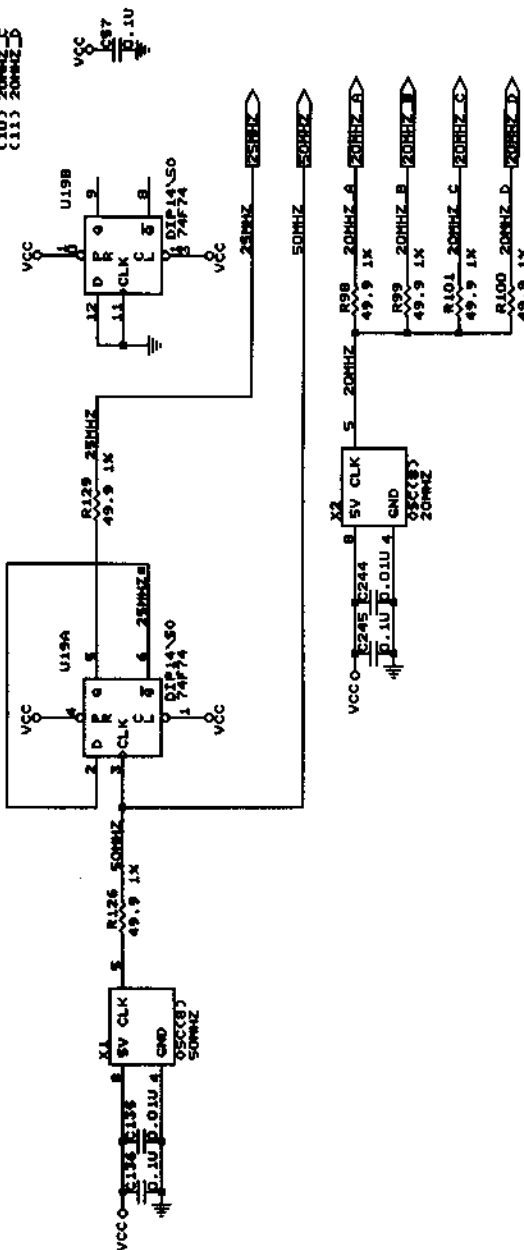
Reset Time = $0.45 \times 220 \times 10^{-6} \times 11(-6) = 1.1(\mu s)$



a relative delayed reset between TSH and QPHY

High Speed Signals:

- (1) 50MHZ
- (2) 25MHZ
- (3) 25MHZ
- (4) 25MHZ
- (5) 25MHZ
- (6) 25MHZ
- (7) 25MHZ
- (8) 25MHZ
- (9) 25MHZ
- (10) 25MHZ
- (11) 25MHZ



(17) RESET & CLOCK

D-Link Corp.

DOC. NO.1

305DES815- - - - A3-

Approved: **18/10/97** Prepared: **18/10/97** Checked: **18/10/97** Issued Date: **18/10/97**

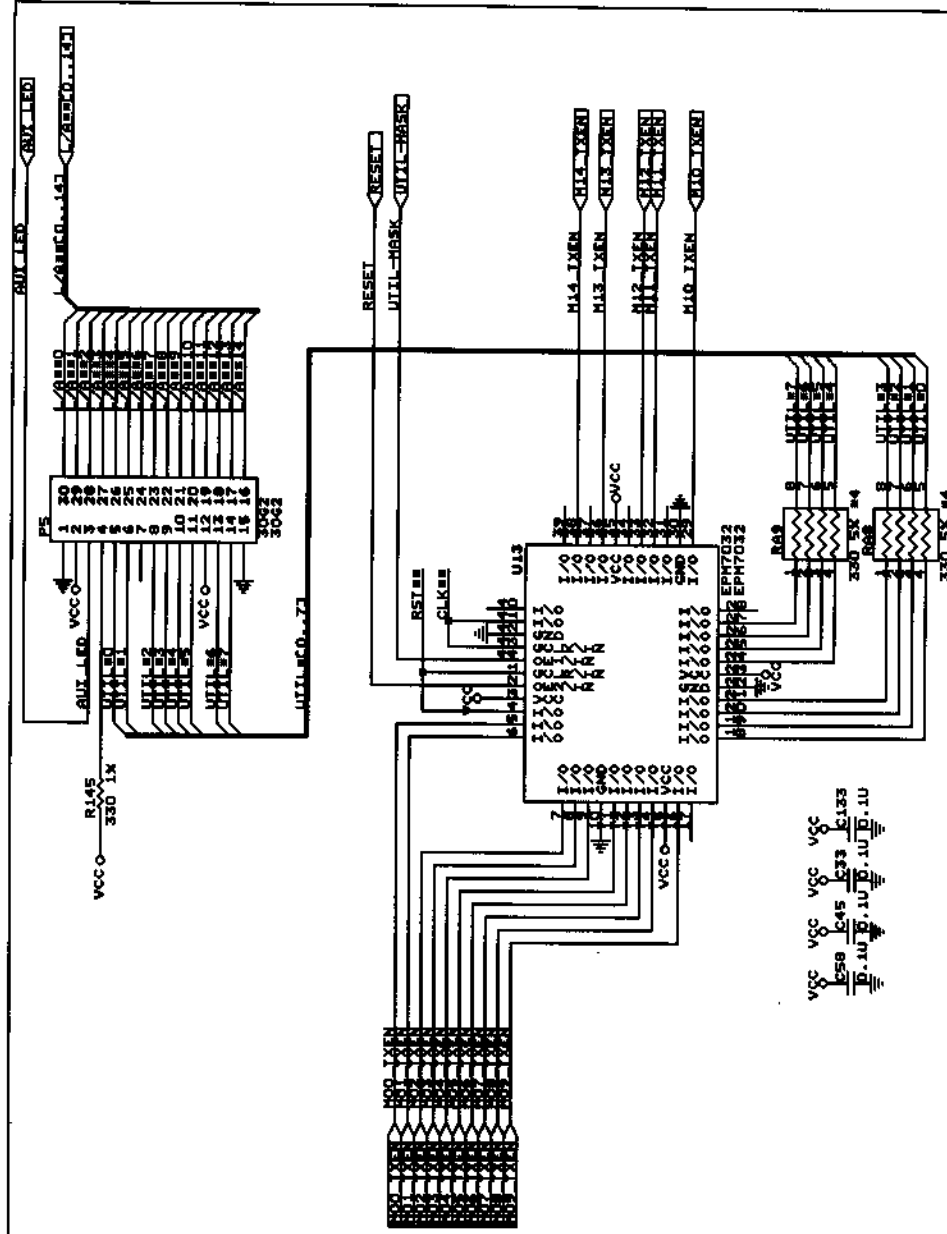
II-Based Ether-Switch

Size Document Number

REV 2100 2200 A3

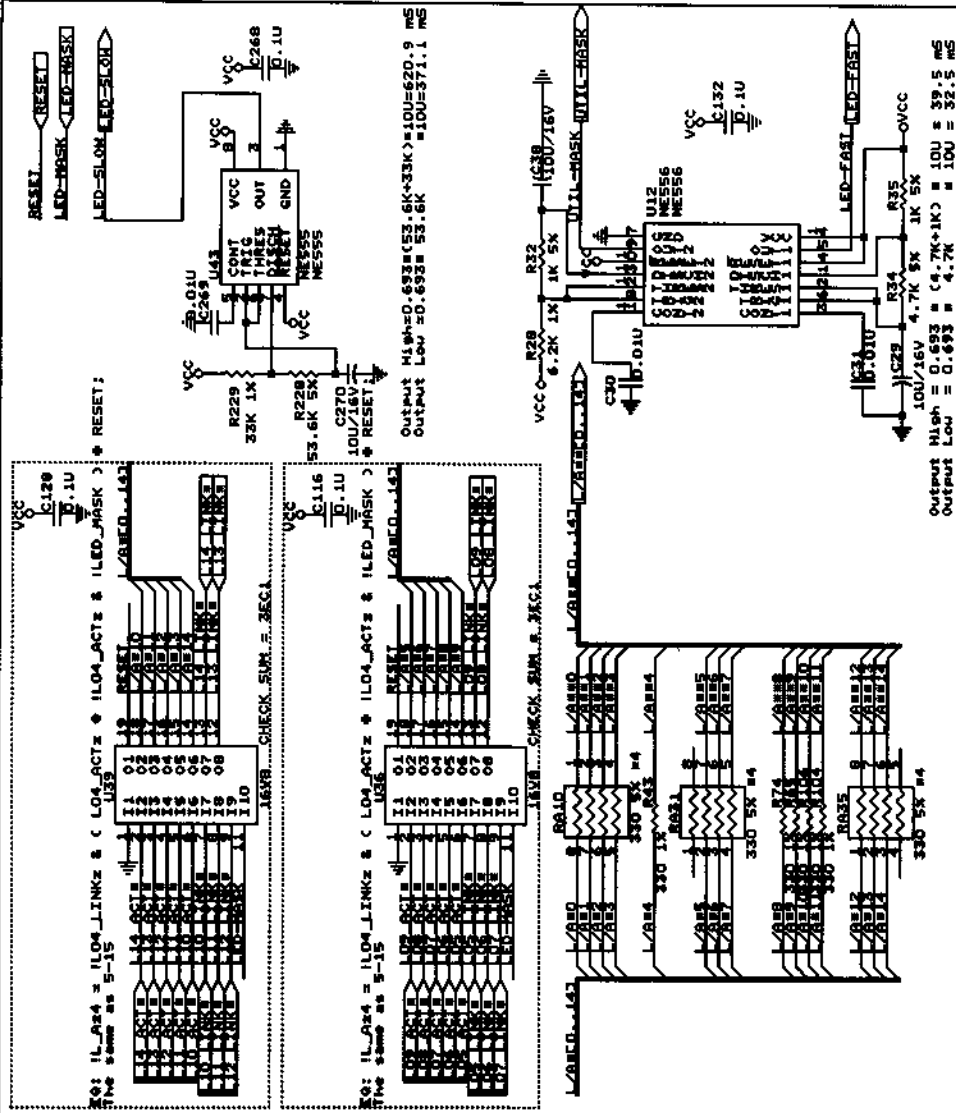
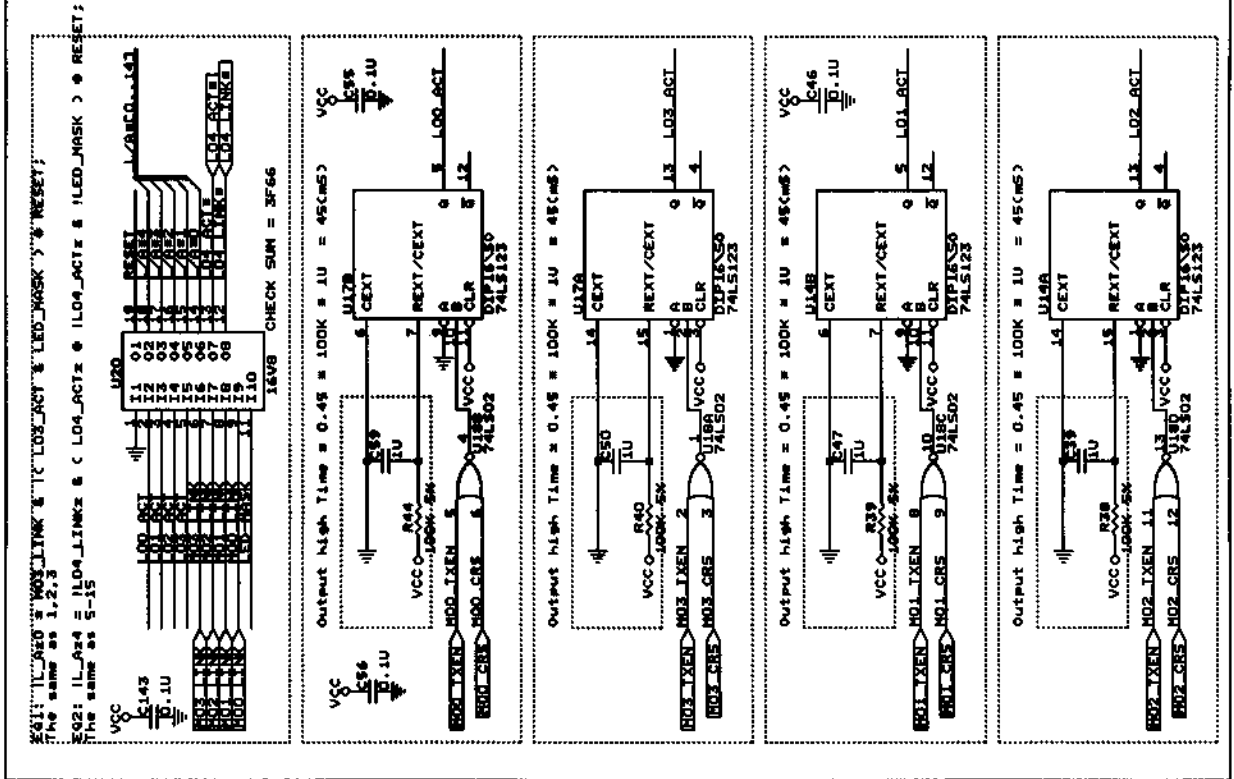
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(Traffic)
(18) DISPLAY-1

D-Link Corp.	Approved: 10/13/96	Prepared: 10/13/96	Checked: 10/13/96
	Doc. No.: 305DES815-...A3-	Issued Date: 10/13/96	Rev: ES-8158
		TI-Based Ether-Switch	2100
		Size/Document Number	2200
		Rev	A3
		Date: Rev 29, 1997	Sheet 18 of 23



(LINK/ACTIVE)
(19) DISPLAY-2

D-Link

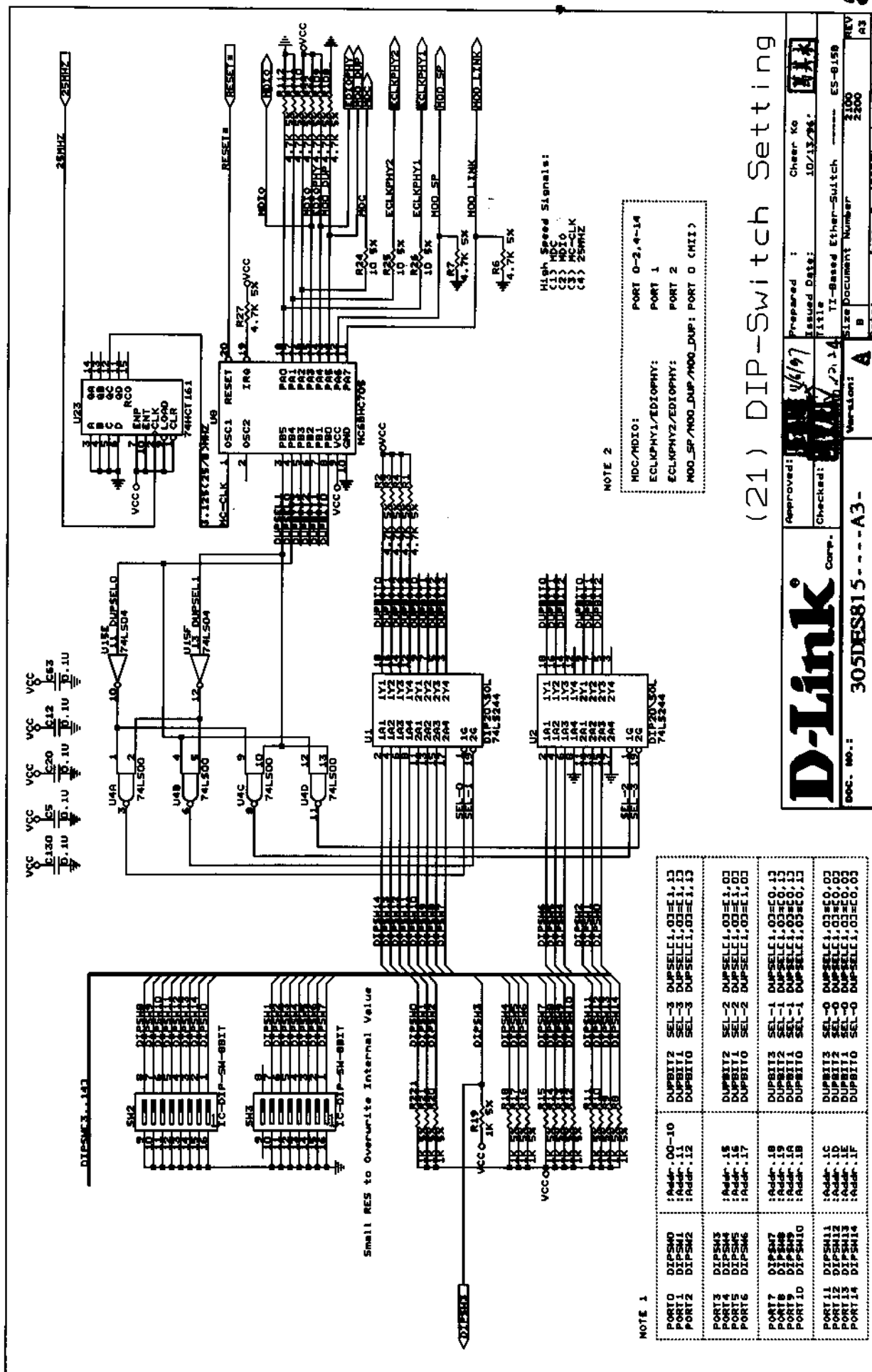
DOC. NO.: 305DES815-----A3-

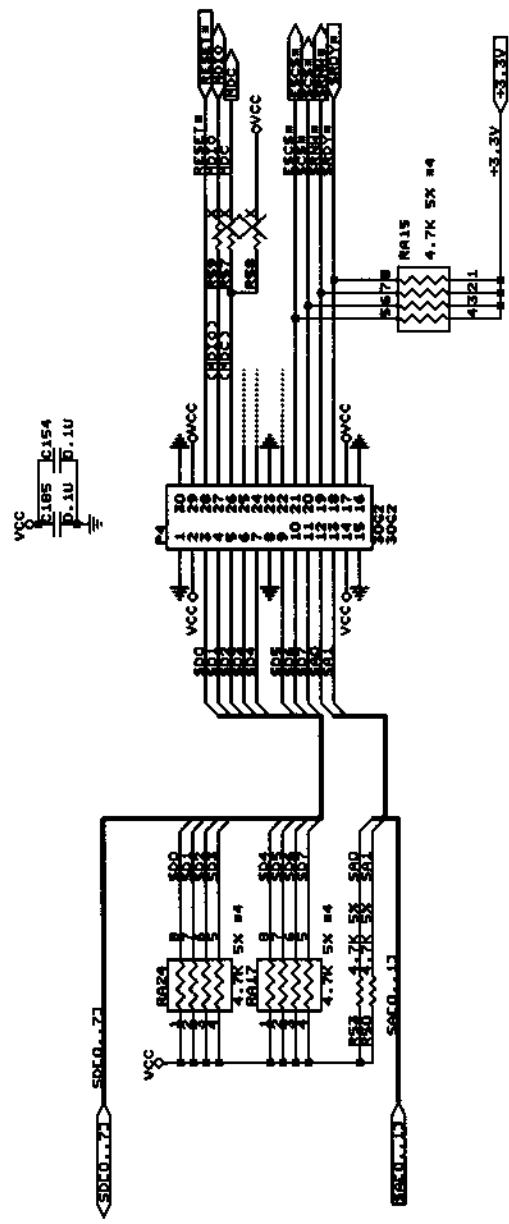
Approved: [Signature]	Prepared: [Signature]	Cheer Ka
	Issued Date: 11/6/97	10/13/96

checked	16.10.24	TI-Based Ether-Switch
Version:	A	size document Number
A2		

Date: June 6, 1997 Sheet

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REV
A3
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X

(22) Parallel Port

D-Link	Approved: 4/6/97		Prepared: 4/6/97	Cheer Ko
	Checked: 4/6/97		Issued Date: 10/13/98	10/13/98
DOC. NO.: 305DES815- - - - A3 -	Version: A		Title: TI-based Ether-Switch	
	Size: 10.4		Size Document Number: ES-915B	
REV A2		REV A2		REV A2
Date: June 2, 1997		Sheet 22 of 23		23

