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生效日期: 87. 4. 30

DES 818 Rev. A2

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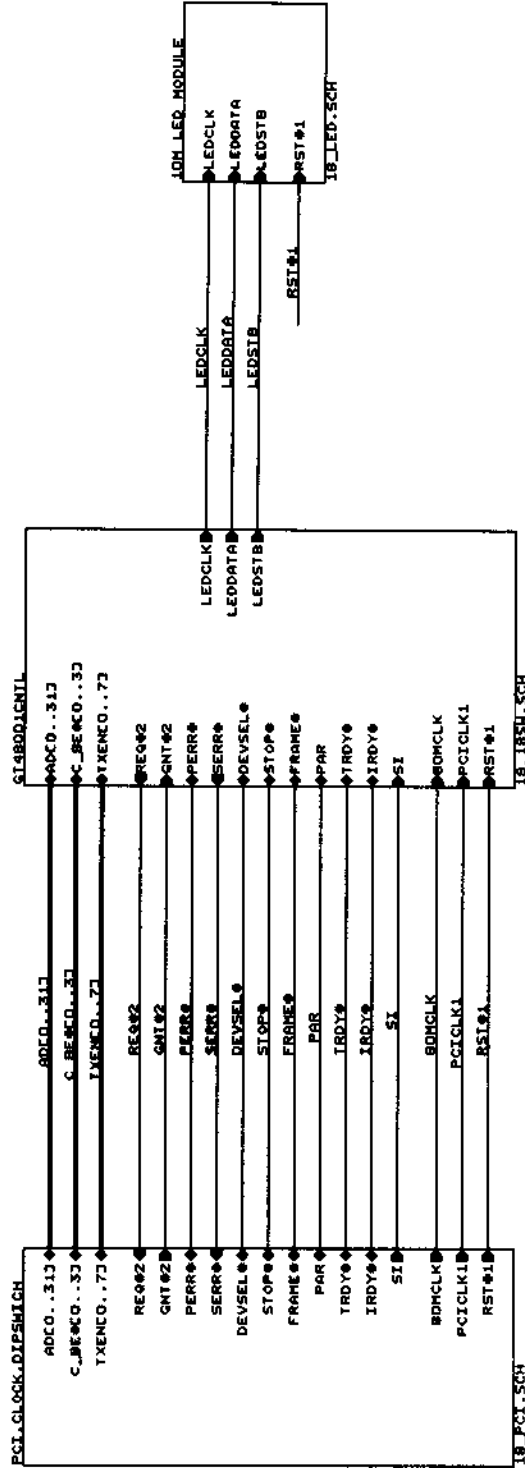
[illegible]

APPROVED	CHECKED	DEPARTMENT	PREPARED
36 徐恩斌 260626		Charles	Polly

DOC NO 305DES818---A2-
VERSION A
PAGE/OF 0-1 / 38

(F050C2)

DES-818's UP_PCB



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DOC. NO.: 305DES818---A2-

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Checked: *[Signature]*

Prepared: POLLY PAN
Issued Date:

DES-818's UP_PCB
Size Document Number
B OEM-0000-00-V10

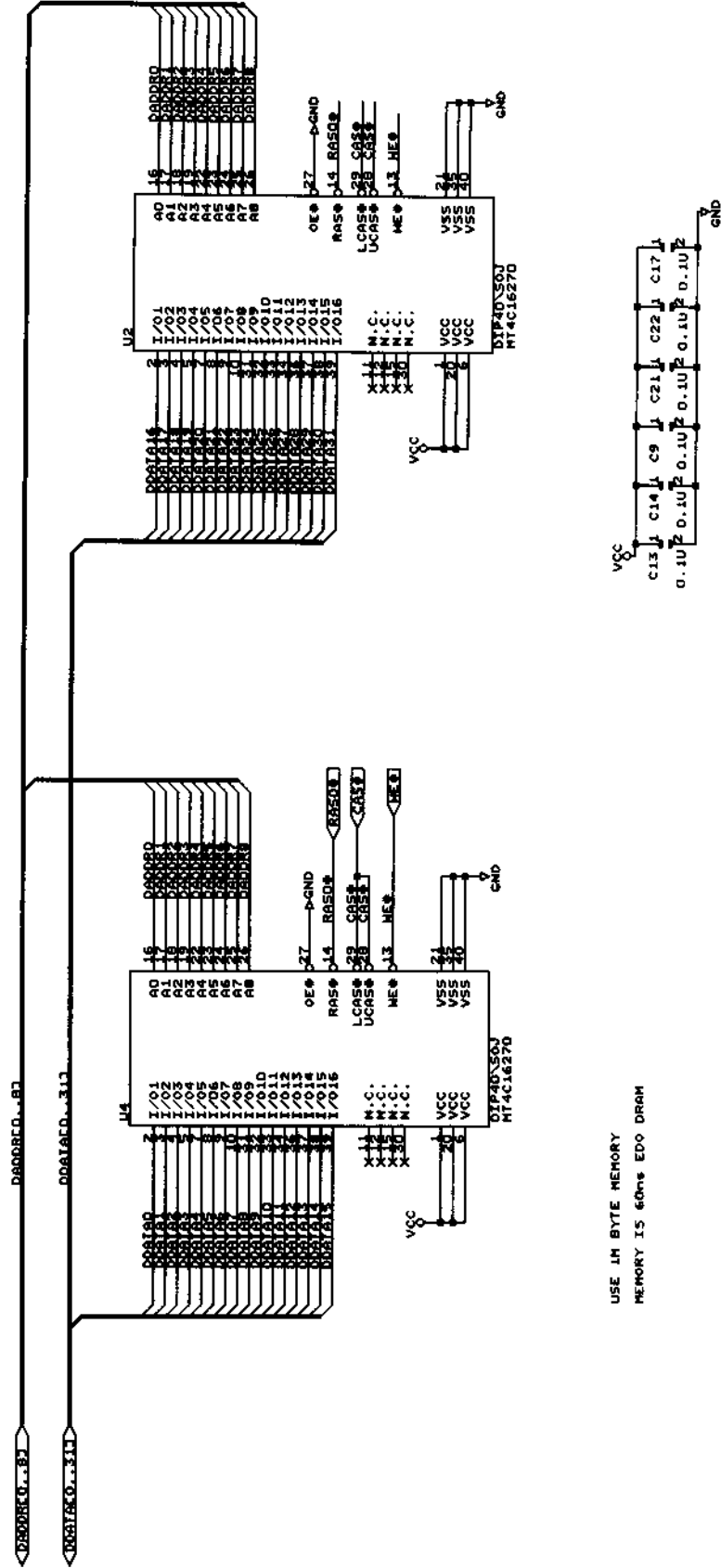
REV
A2
1 of 12

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Approved: <i>[Signature]</i>	Prepared: <i>[Signature]</i>	DES-018* UP_PCB
2/20/98	: POLLY PAN	
	Issue Date:	
	Title	
	Size	Number
	B	OEM-0000-00-V10
	Date	February 16, 1998
	Version:	A

DRAM FOR GT48001



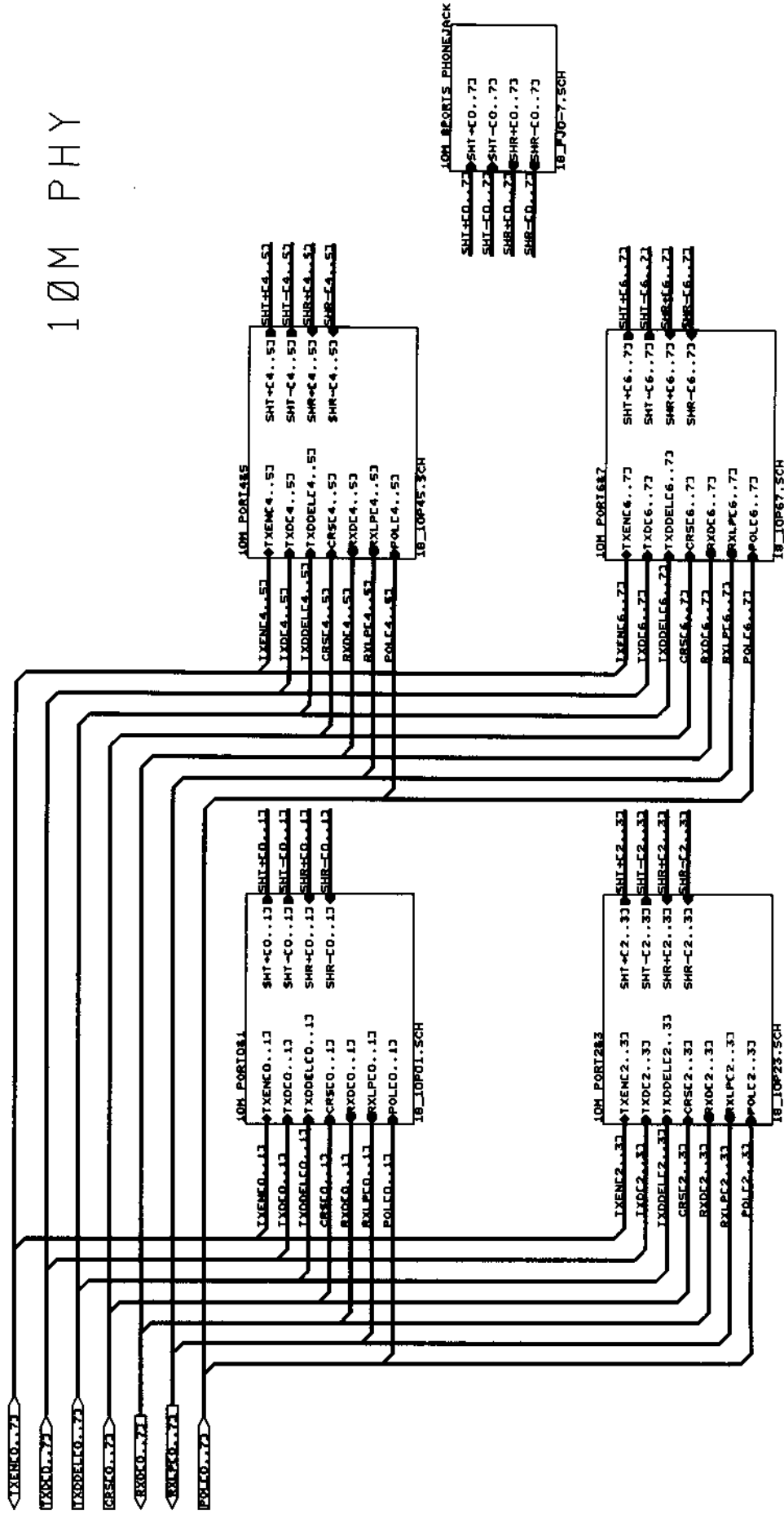
D-Link Corp.

DOC. NO.: 305DES18-A2

Approved: *[Signature]*
Checked: *[Signature]*
Title: DES-818's UP_FCB

REV	Size	Document Number	Date	Page
A2	B	DEM-0000-00-V10	February 15, 1998	3 of 12

10M PHY



IOM_PORT18 PHONELACK
 SHT-CO..73
 SHT-CO..73
 SHR-CO..73
 SHR-CO..73
 SHR-CO..73
 SHR-CO..73

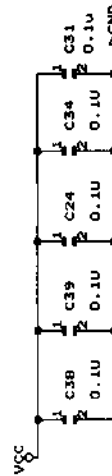
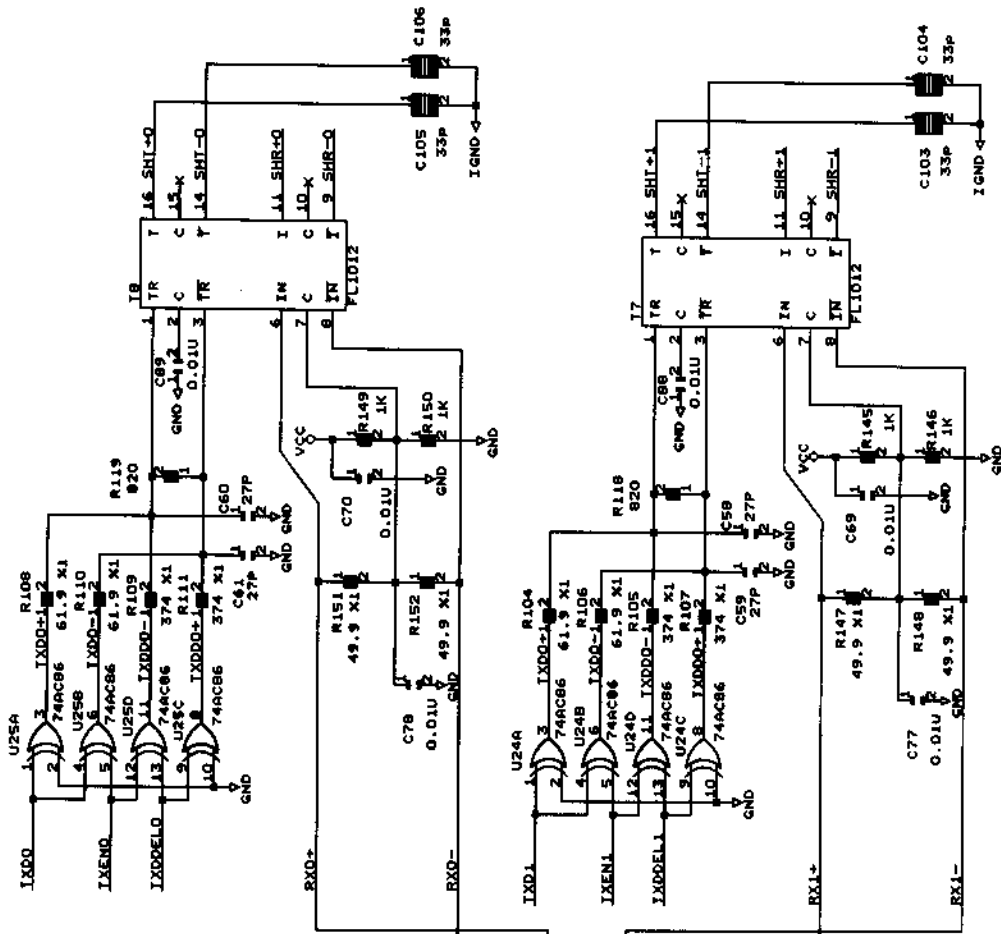
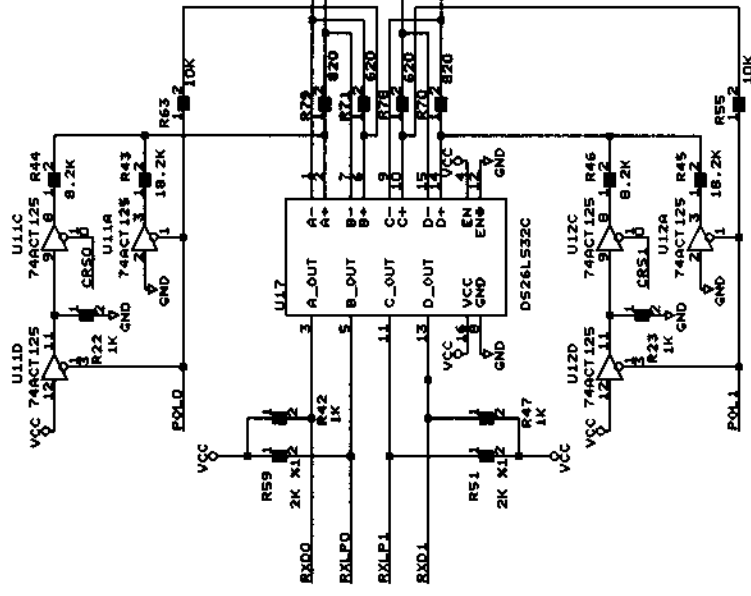
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 Checked: *[Signature]*
 Version: A
 Size Document Number: 8
 Date: February 16, 1998
 Title: DES-818's UP_PCB

REV
 A2
 4 of 12

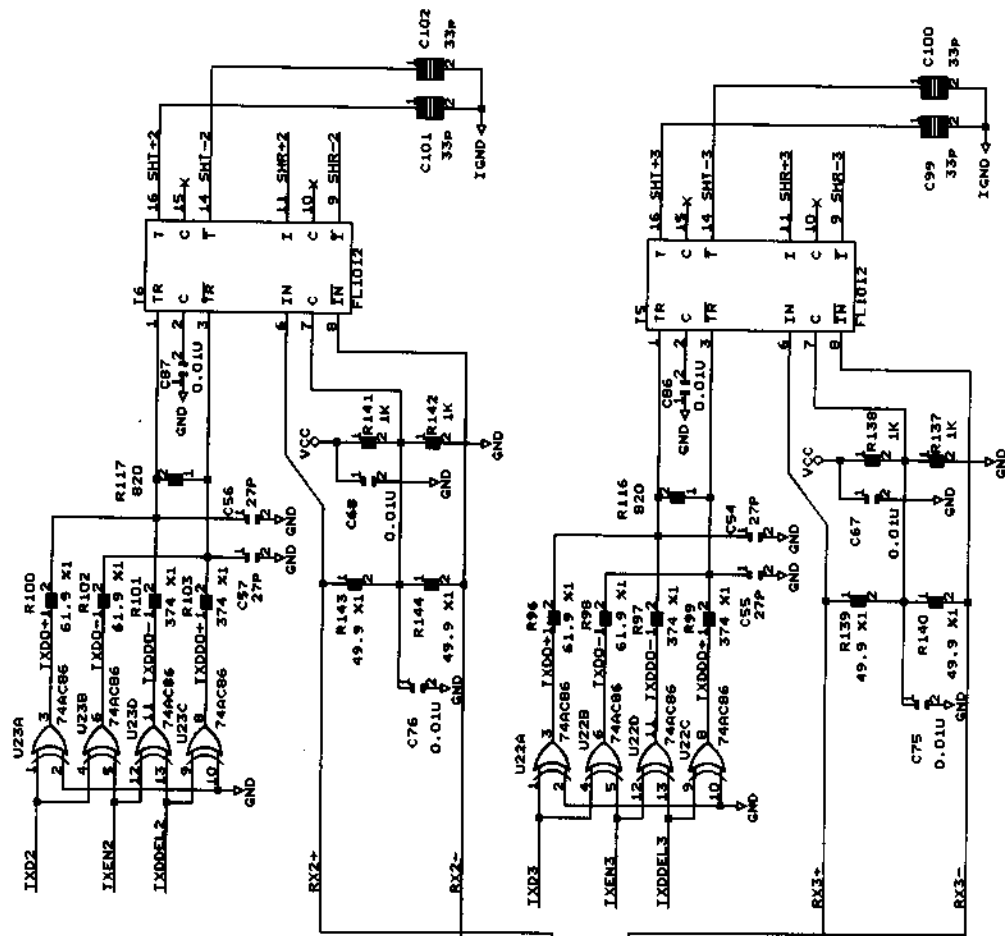
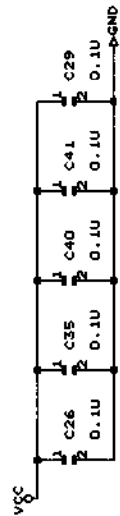
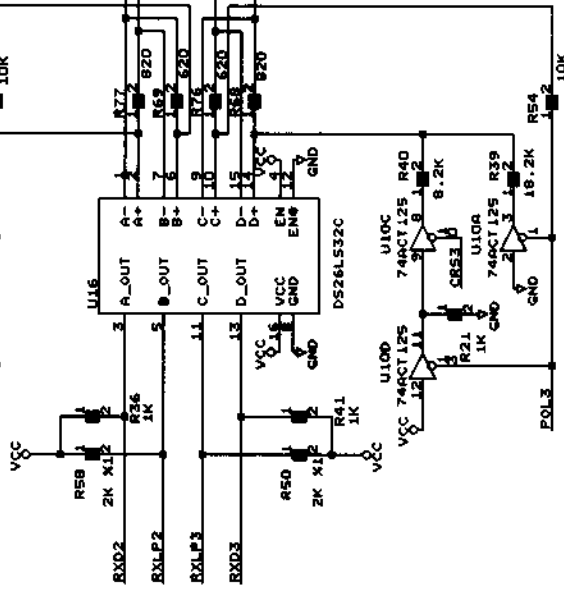
10M PHY PORT0-1



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DOC. NO.:	305DES18- --A2-
Version:	A
Size Document Number	DES-818's UP_PCB
REV	42
Date:	February 15, 1998
Sheet	5 of 12



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DOC. NO.: 305DES818- - - -A2-

Approved: 張福如 Prepared: 張福如 : POLLY PAN
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Checked: 張福如 Title: 1/1

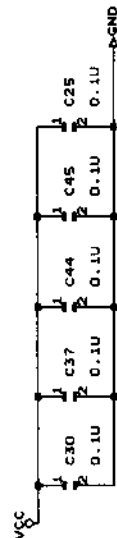
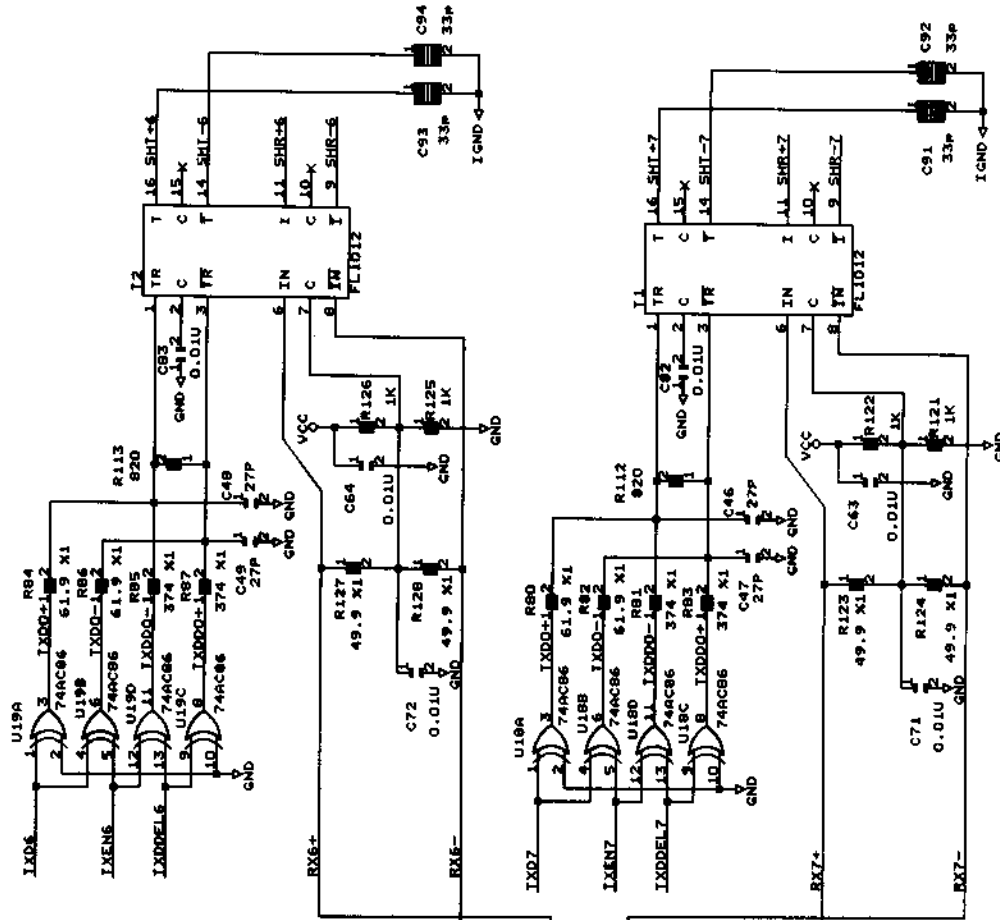
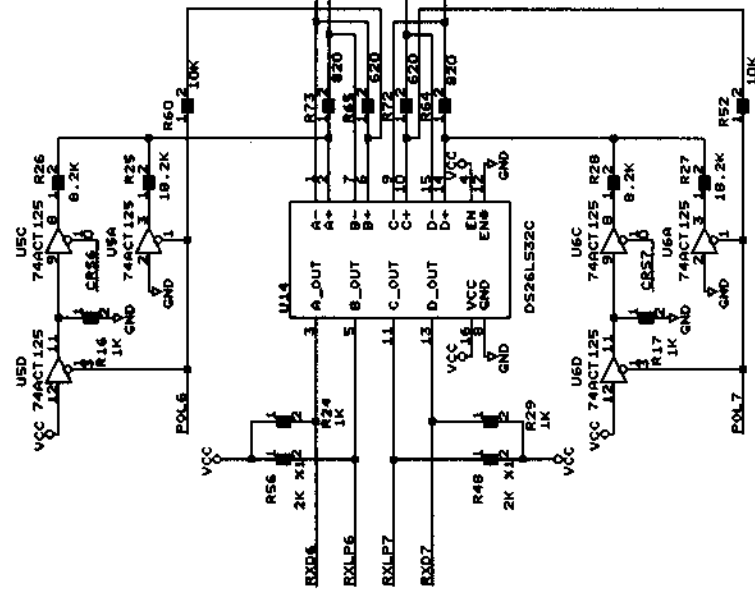
DES-810/DES-818, LOW PCB

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Date:	February 16, 1998		
Sheet	3		

10M PHY PORT6-7



SMART SQUELCH CIRCUIT

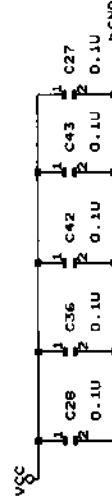
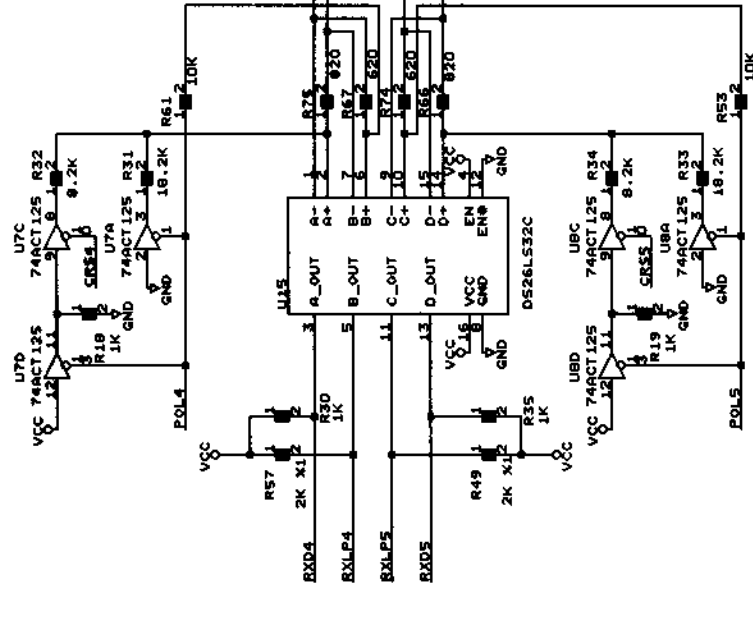




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DOC. NO.: 305DES18-A2

Approved: <i>[Signature]</i>	Prepared: POLLY PAN
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Version: A	Title: DES-810/DES-818's LON_PCB
Size: 110	Size Document Number: OEM-0000-00-V10
Date: February 15, 1998	Rev: 7 of 12

Figure 1 shows a schematic representation of a 12000 bp DNA fragment. The fragment is divided into 12 regions, each with a unique restriction enzyme site. The regions are labeled as follows: 1. XbaI (X) 2. XbaI (X) 3. XbaI (X) 4. XbaI (X) 5. XbaI (X) 6. XbaI (X) 7. XbaI (X) 8. XbaI (X) 9. XbaI (X) 10. XbaI (X) 11. XbaI (X) 12. XbaI (X). The fragment is flanked by XbaI sites. The regions are numbered 1 to 12 from left to right. The fragment is 12000 bp long.

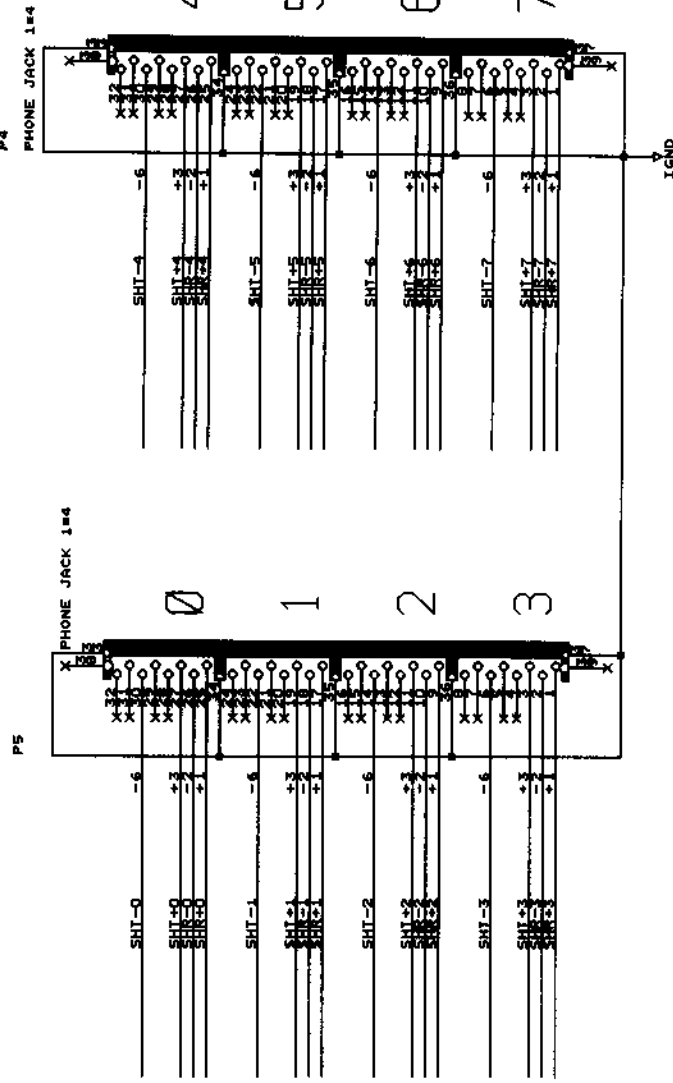


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	Title	

DFS-BIO-MES-010

Size	Document Number
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SHT-00...72
 SHT-00...72
 SHT-00...72
 SHT-00...72



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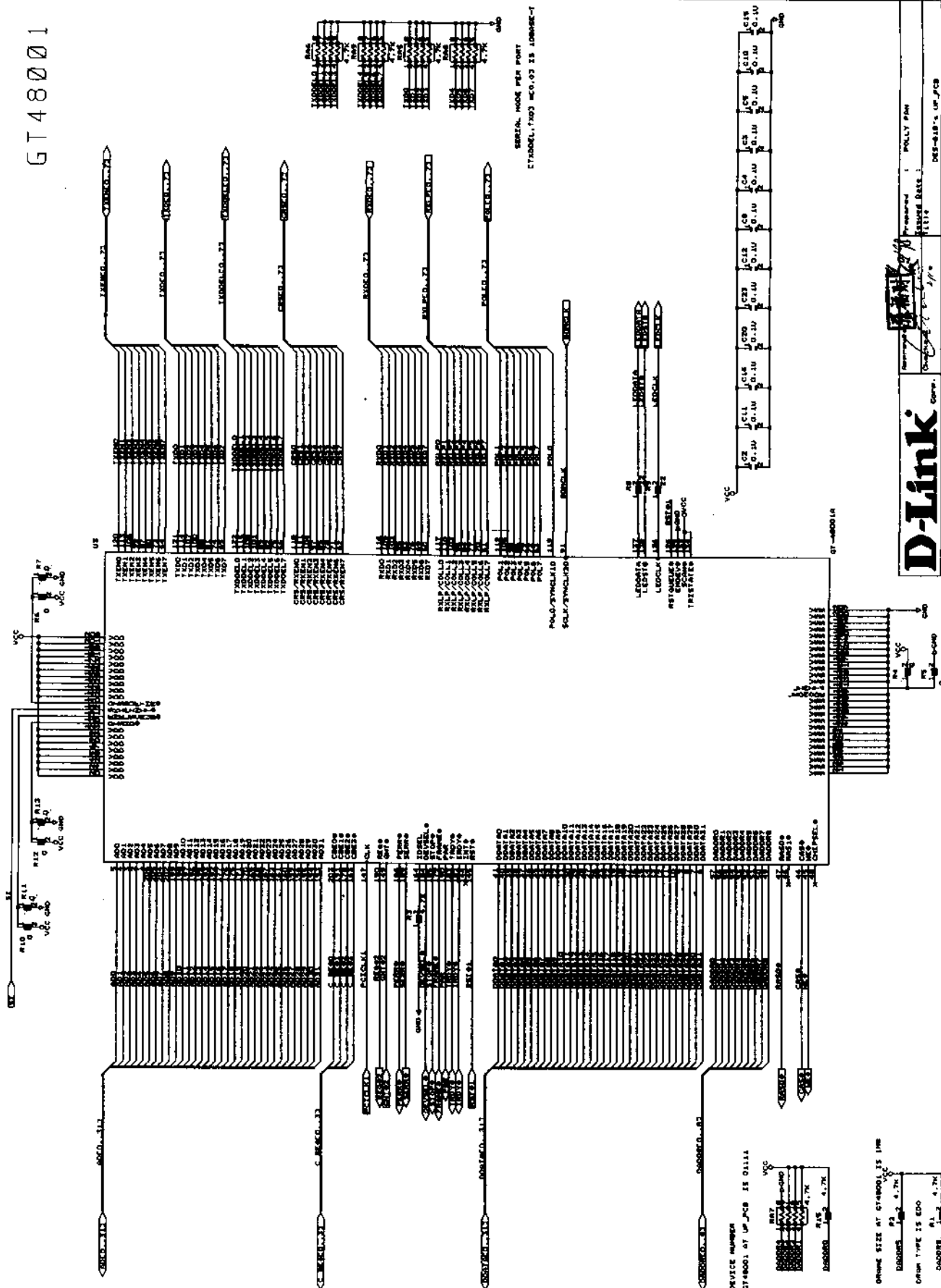
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Version: A Size Document Number: DES-818's UP_PCB

DOC. NO.: 305DES818-...-A2- Date: February 15, 1998

REV: A2

GT48001

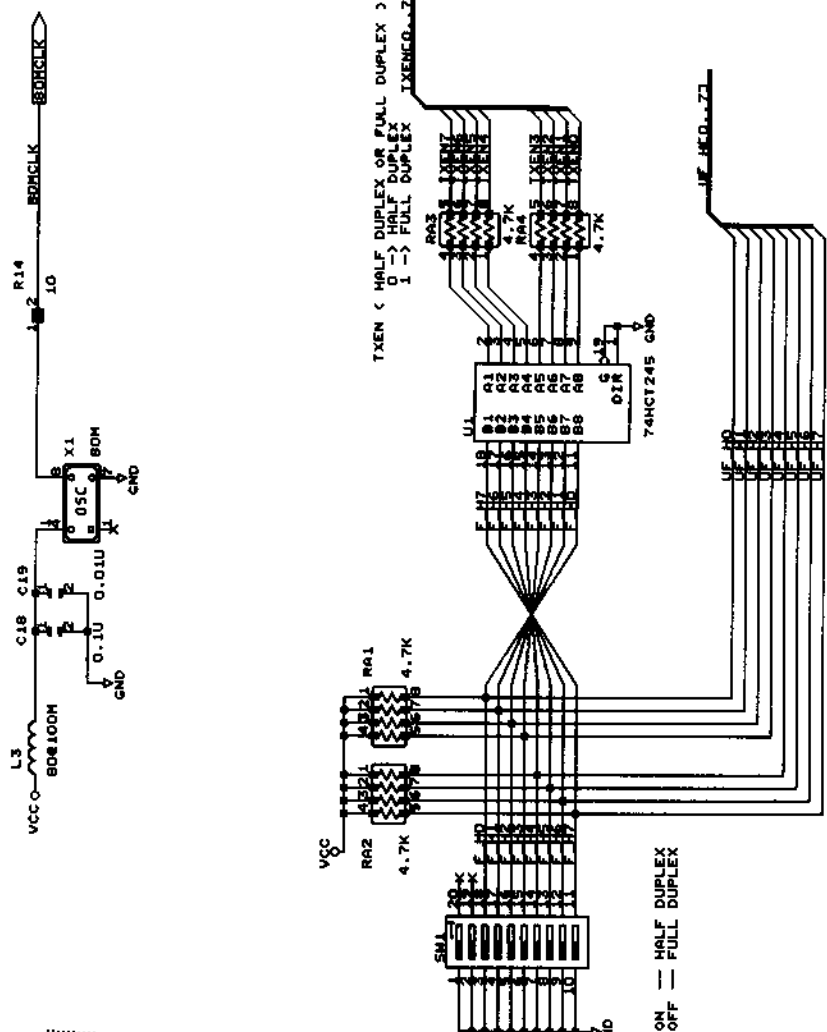
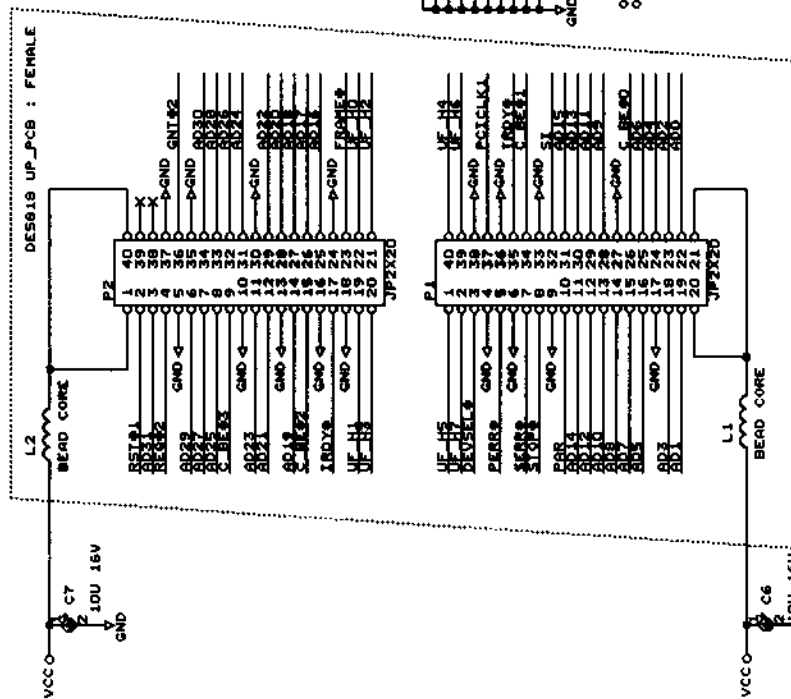


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Part Number: 305DES818 - A2 - Version: A

10/33

PCI INTERFACE



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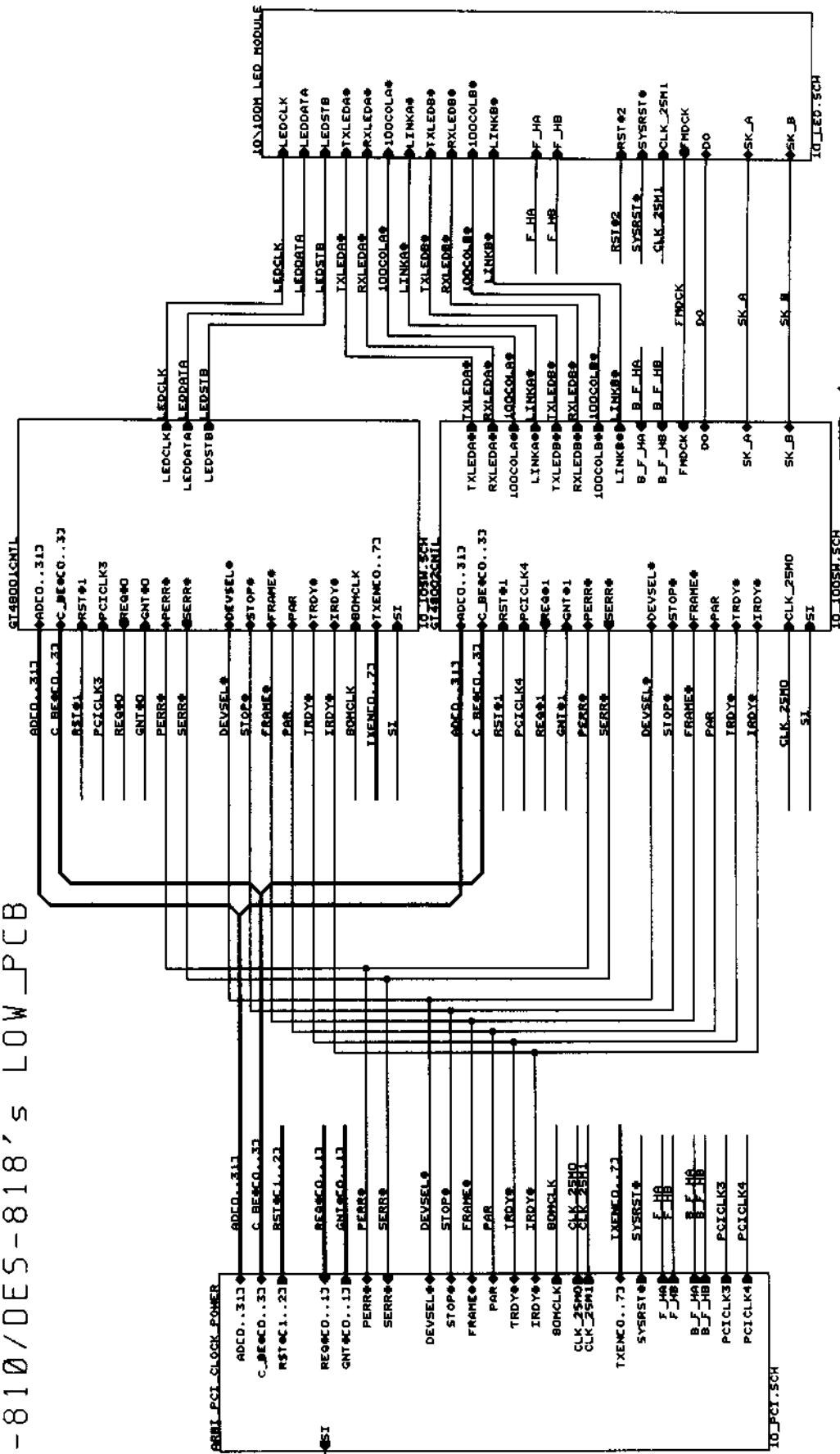
DOC. NO.: 305DES818 -- A2

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 Issued Date: *[Date]*
 Title: DES-818's UP_PCB

Version: A
 Size Document Number: B
 Date: February 16, 1998
 Sheet 12 of 12

12/83

DES-810/DES-818's LOW_PCB



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 Title: DES-810/DES-818's LOW_PCB
 Size: Document Number
 Date: February 15, 1998
 Sheet: 1 of 2

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 Issued Date:
 Title: DES-810/DES-818's LOW_PCB
 Size: Document Number
 Date: February 15, 1998
 Sheet: 1 of 2

The diagram illustrates the timing and signal flow for the IOM_51600 controller and its physical interface. The signals are categorized into four groups: DATA, ADDRESS, COMMAND, and STATUS.

- DATA Signals:** AD[0..31], C_BE[0..3], RST[0], RST[1], RST[2], RST[3], RST[4], RST[5], RST[6], RST[7], RST[8], RST[9], RST[10], RST[11], RST[12], RST[13], RST[14], RST[15], RST[16], RST[17], RST[18], RST[19], RST[20], RST[21], RST[22], RST[23], RST[24], RST[25], RST[26], RST[27], RST[28], RST[29], RST[30], RST[31].
- ADDRESS Signals:** AD[0..31], C_BE[0..3], RST[0], RST[1], RST[2], RST[3], RST[4], RST[5], RST[6], RST[7], RST[8], RST[9], RST[10], RST[11], RST[12], RST[13], RST[14], RST[15], RST[16], RST[17], RST[18], RST[19], RST[20], RST[21], RST[22], RST[23], RST[24], RST[25], RST[26], RST[27], RST[28], RST[29], RST[30], RST[31].
- COMMAND Signals:** AD[0..31], C_BE[0..3], RST[0], RST[1], RST[2], RST[3], RST[4], RST[5], RST[6], RST[7], RST[8], RST[9], RST[10], RST[11], RST[12], RST[13], RST[14], RST[15], RST[16], RST[17], RST[18], RST[19], RST[20], RST[21], RST[22], RST[23], RST[24], RST[25], RST[26], RST[27], RST[28], RST[29], RST[30], RST[31].
- STATUS Signals:** AD[0..31], C_BE[0..3], RST[0], RST[1], RST[2], RST[3], RST[4], RST[5], RST[6], RST[7], RST[8], RST[9], RST[10], RST[11], RST[12], RST[13], RST[14], RST[15], RST[16], RST[17], RST[18], RST[19], RST[20], RST[21], RST[22], RST[23], RST[24], RST[25], RST[26], RST[27], RST[28], RST[29], RST[30], RST[31].

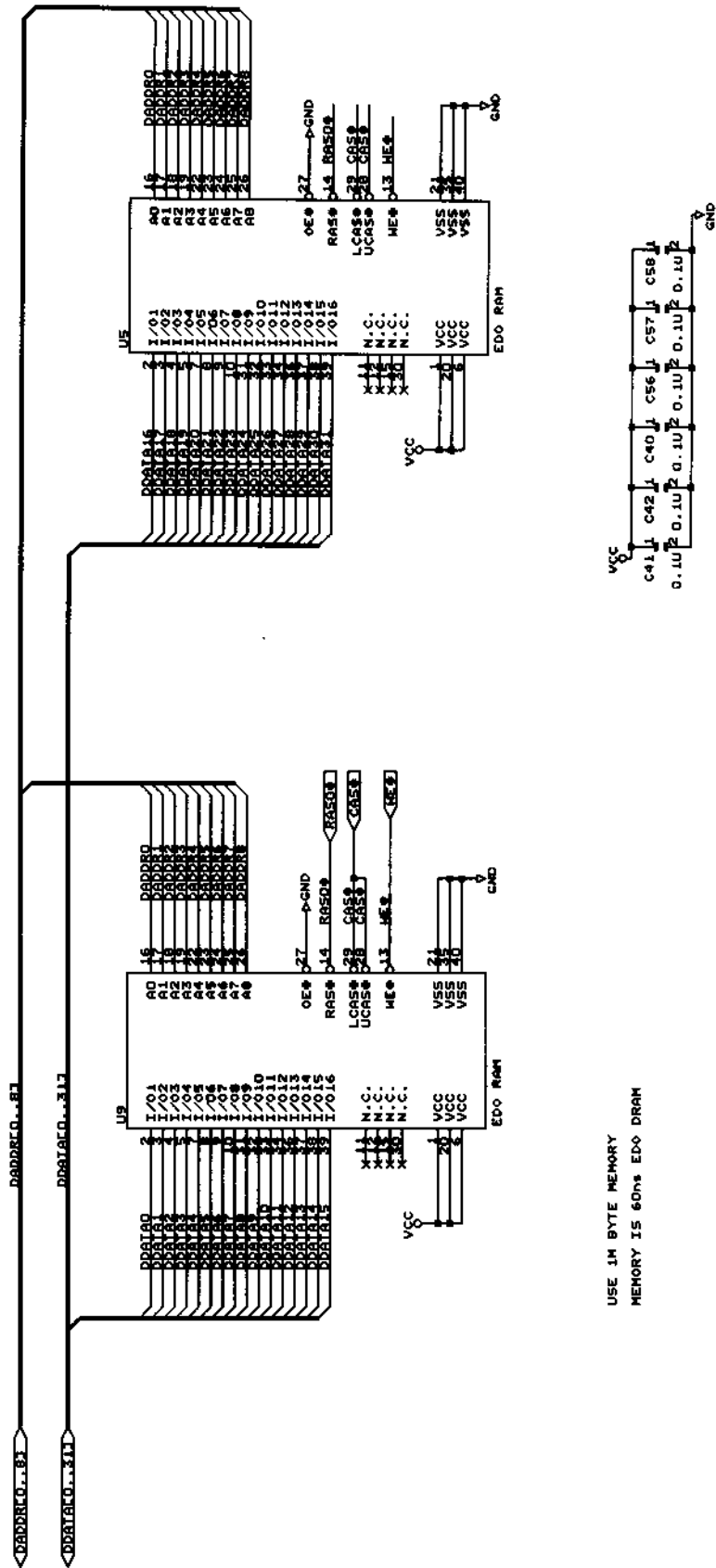
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Checked: <i>[Signature]</i>	Issued Date: <i>2/12/78</i>	Title

DOC. No.: 305DES818---A2-

Version: A	Size Document Number	REV
	8	A2
	OEM-0000-00-V10	
Date: February 16, 1998	Sheet	2 of 21

DRAM FOR GT48001



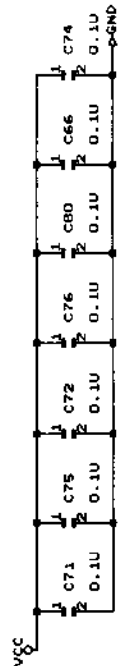
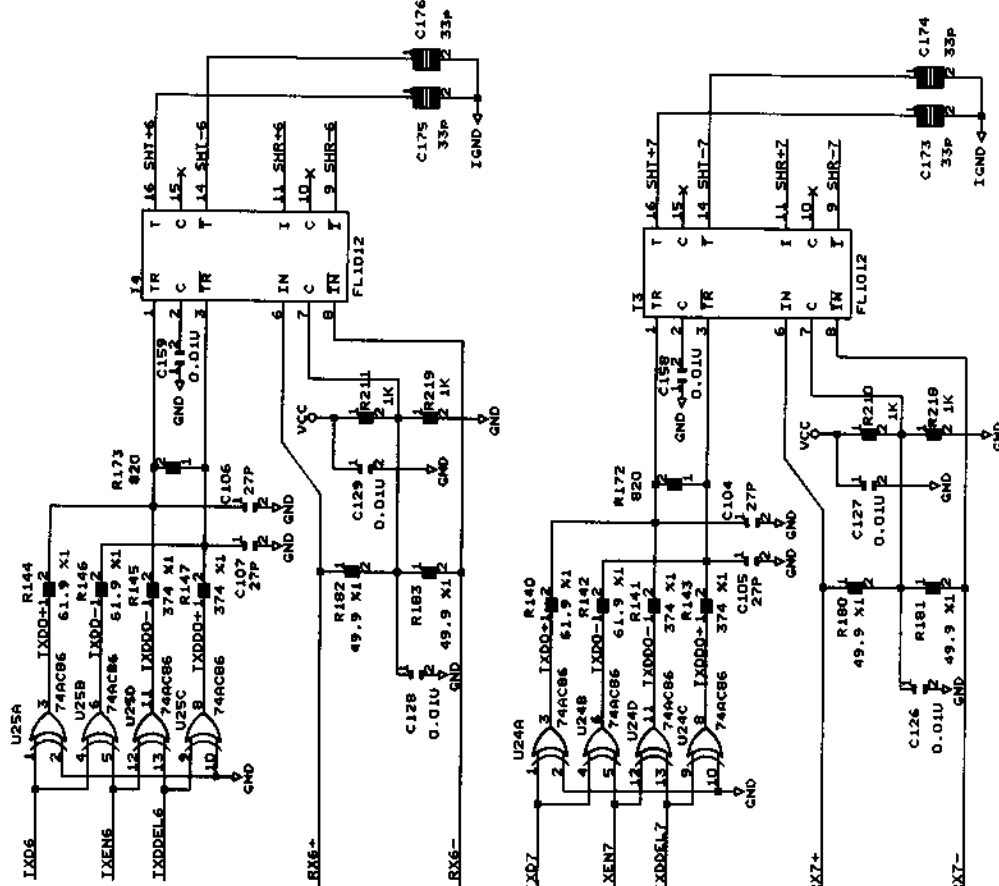
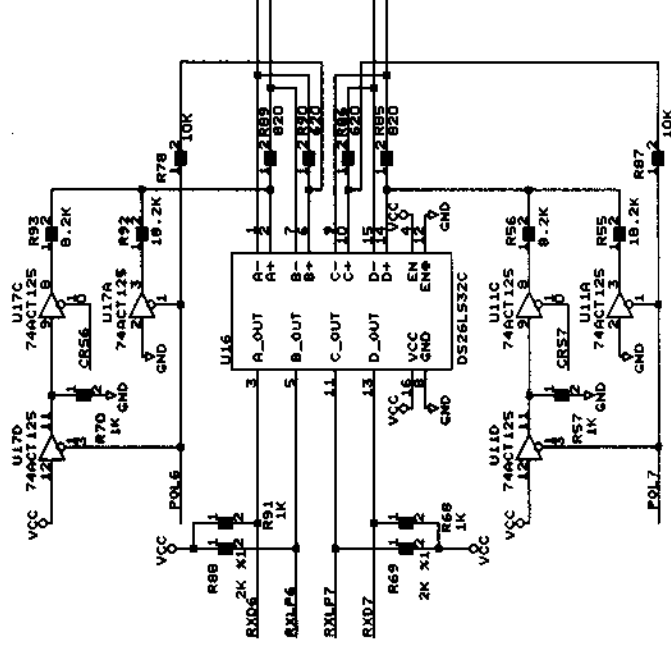
USE 1M BYTE MEMORY
MEMORY IS 60ns EDO DRAM

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DOC. NO.: 305DES18-...-A2
Version: A
Title: DES-810/DES-818's LOW_PCB
Size Document Number: B
REV: A2
Date: February 16, 1998
3 of 21

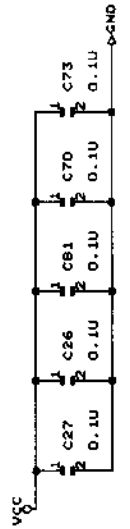
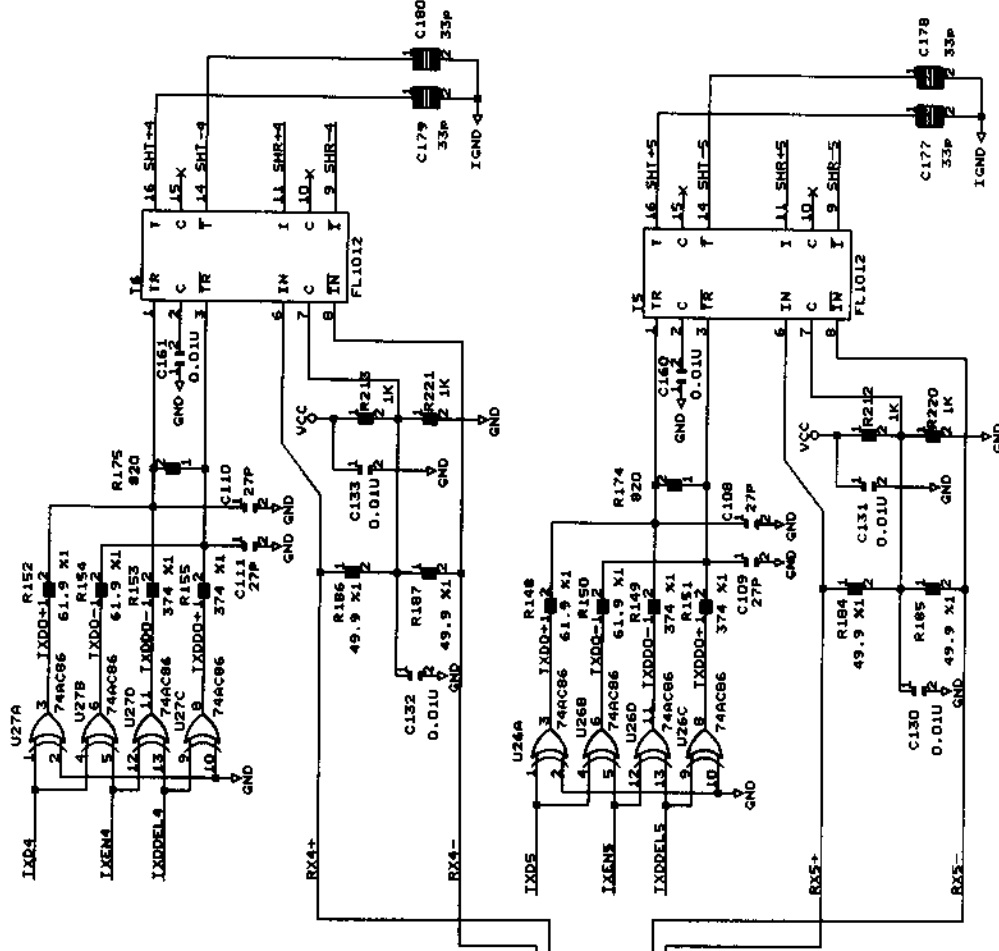
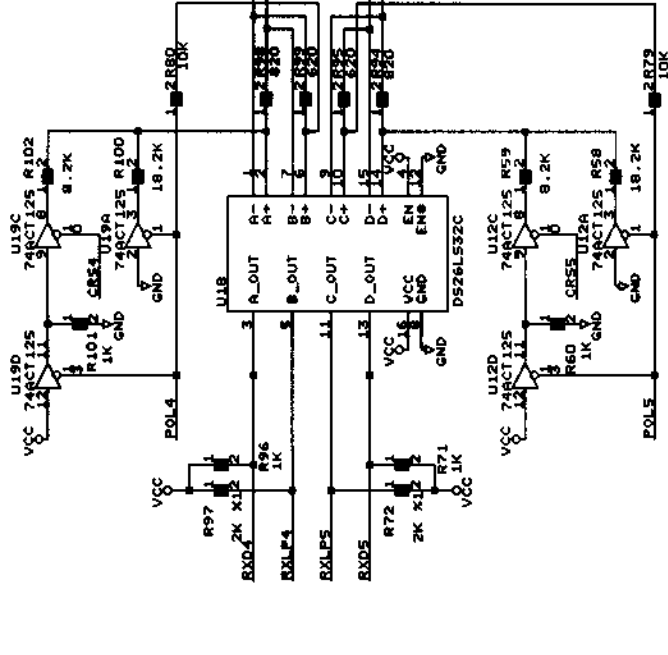
10M PHY PORT6-7



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 305DES18-A2-
 Version: A
 Date: February 16, 1998
 Sheet 7 of 21

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 Size Document Number: 0EM-0000-00-V10
 Date: February 16, 1998
 Sheet 7 of 21

10M PHY PORT4-5



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DOC. NO.: 305DES18-A2

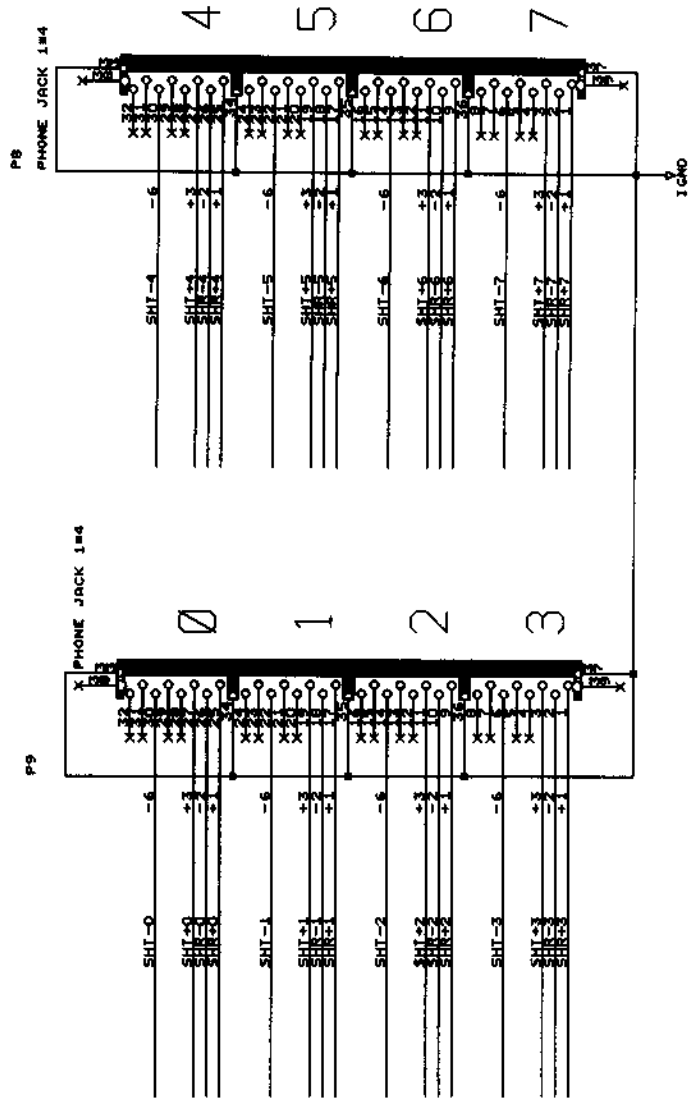
Prepared: POLLY PAN
Issued Date:
Title

DES-810/DES-818's LOW PCB
Size Document Number
OEM-0000-00-V10
Date: February 15, 1998

REV
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21

20/38

SHT-C0..73
 SHT-C0..73
 SHR-C0..73
 SHR-C0..73



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DOC. NO.: 305DES818- - -A2-

Prepared: POLLY PAN
 Issued Date:

Title: DES-810/DES-818's LOW_PCB

Size Document Number
 B OEM-0008-PA-V11
 Date: February 16, 1999

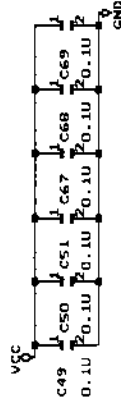
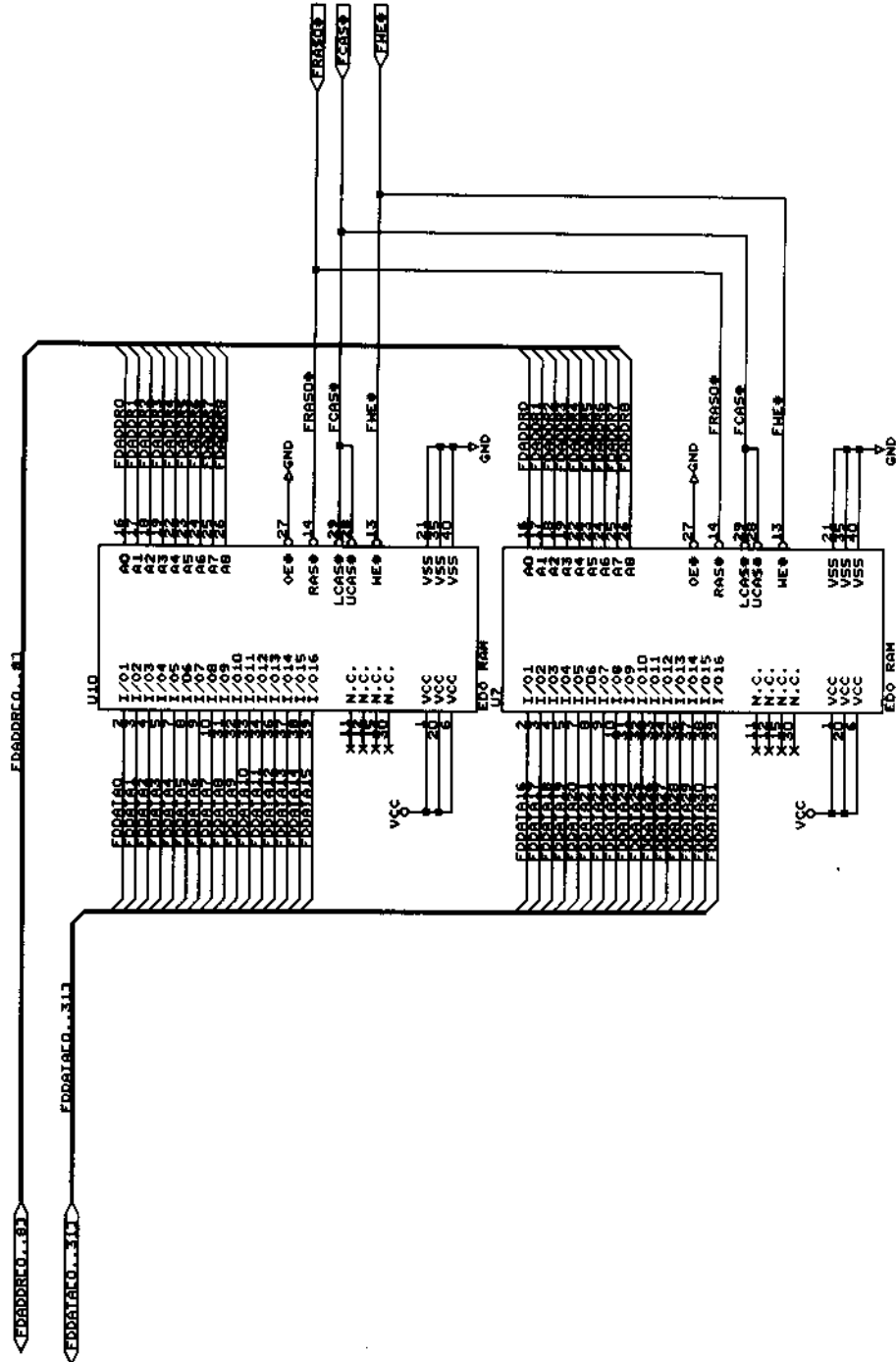
Version: A
 REV 02
 9 of 21

[illegible]

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Checked: <i>[Signature]</i>	Issued Date: <i>[Signature]</i>	
Compl. <i>210</i>	Title	DES-810/DES-818's
	Version: <i>1</i>	Size Document Number
		B
		DEM-0000-00-0000
		Date: FEBRUARY 15, 1988

DOC. NO.: 305DES818- -A2-	Version:
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	Size Document Number
B	OEM-0000-OO-
Date: February 16, 1998	

DRAM FOR GT48002 IS 1MB



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Prepared: POLLY PAN
Issued Date: *[Date]*

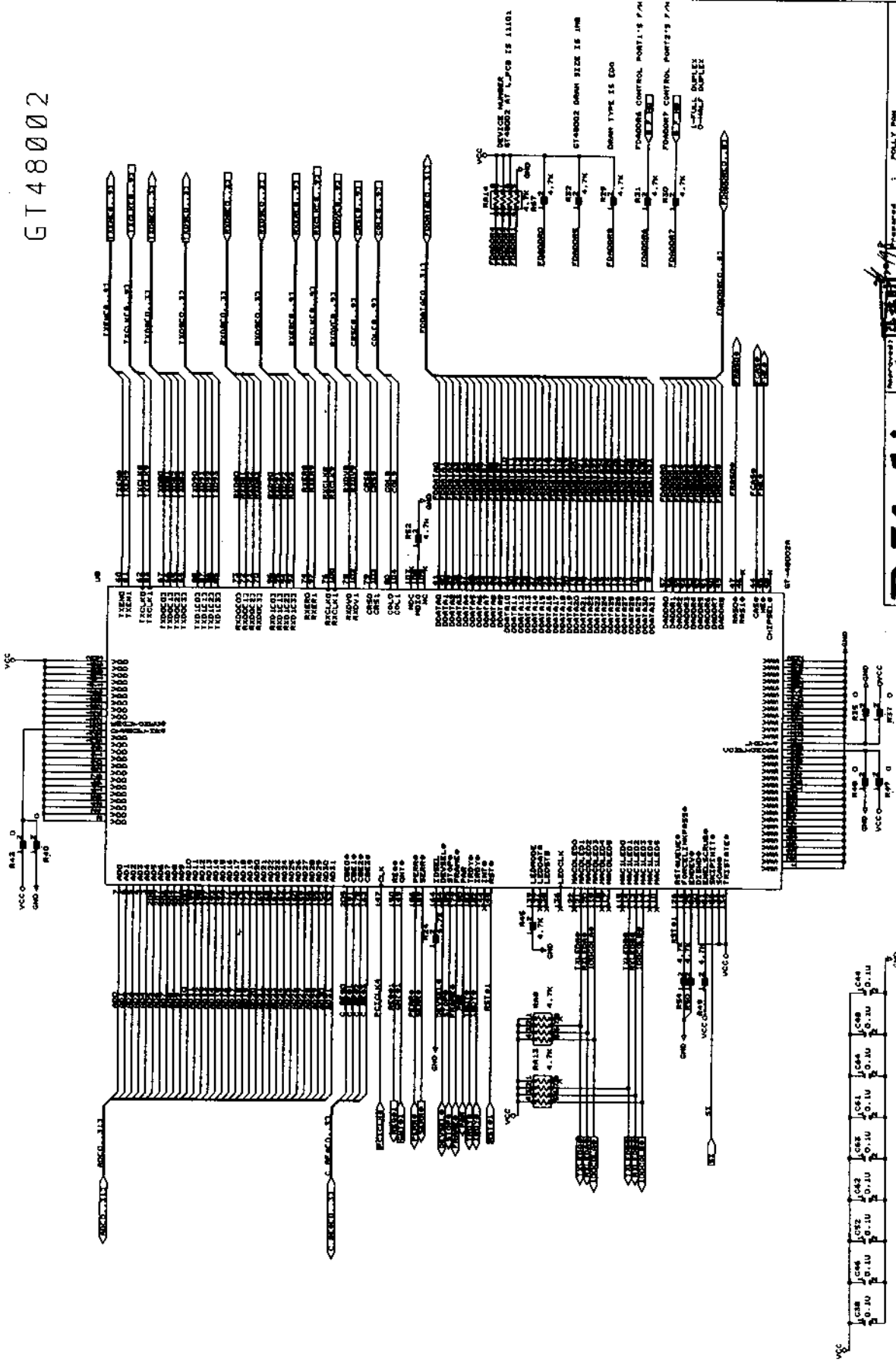
DOC. NO.: 305DES818 - - - A2 -

DES-810/DES-818's LOW PCB
OEM-0000-00-V10

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A2
21

24/33

GT48002



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Part No.: 305DES818-A2

Rev: 1.0

DATE: 1998.05.15

DESIGNED BY: [Signature]

CHECKED BY: [Signature]

APPROVED BY: [Signature]

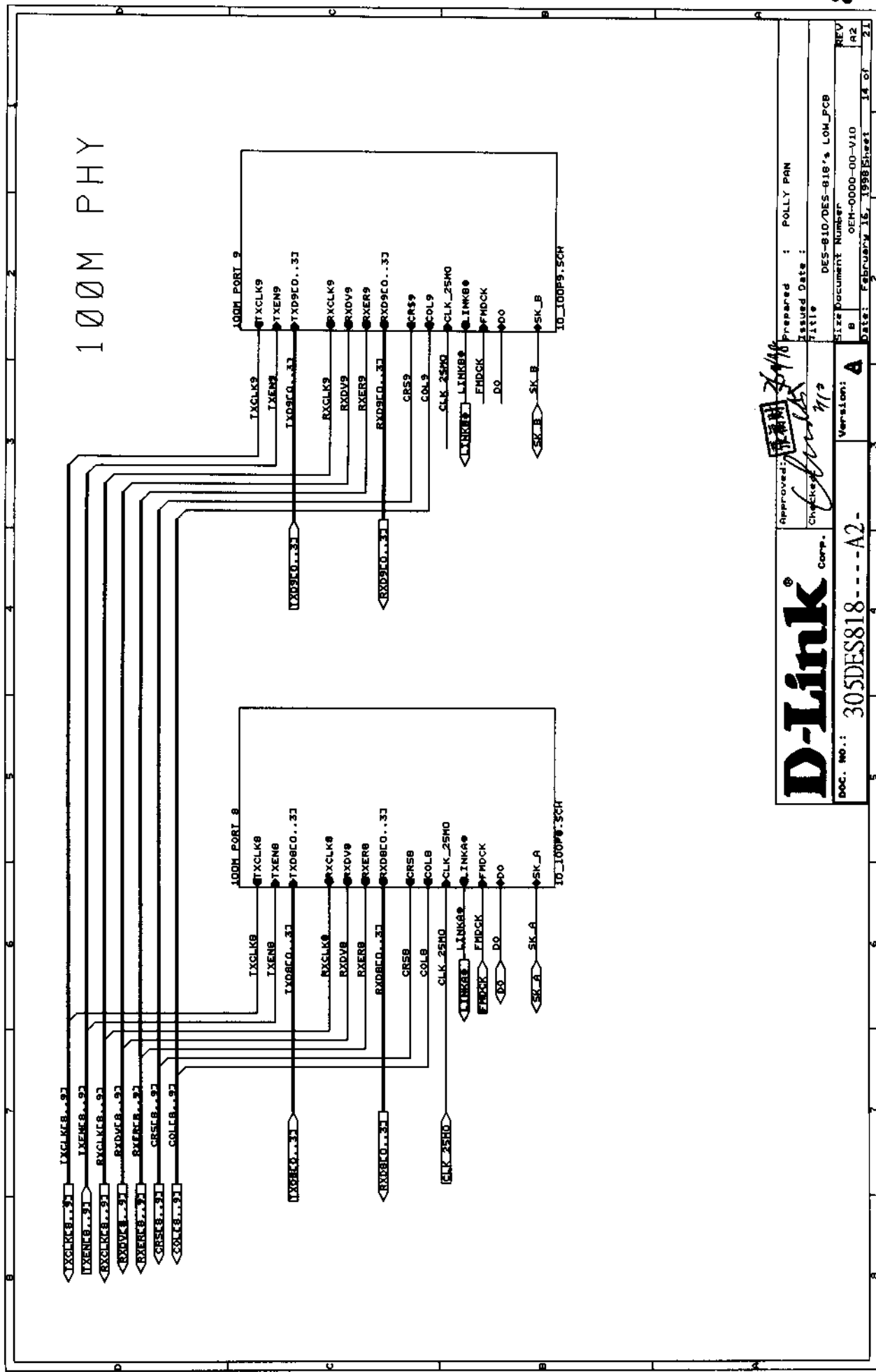
DATE: 1998.05.15

DESIGNED BY: [Signature]

CHECKED BY: [Signature]

APPROVED BY: [Signature]

DATE: 1998.05.15



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3/12

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DOC. NO.: 305DES18- ---A2-

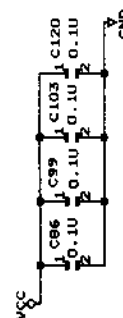
Approved: *[Signature]*
Checked: *[Signature]*
Title: DES-810/DES-818's LOW_PCB

Version: A

Prepared: POLLY PAN
Issued Date:

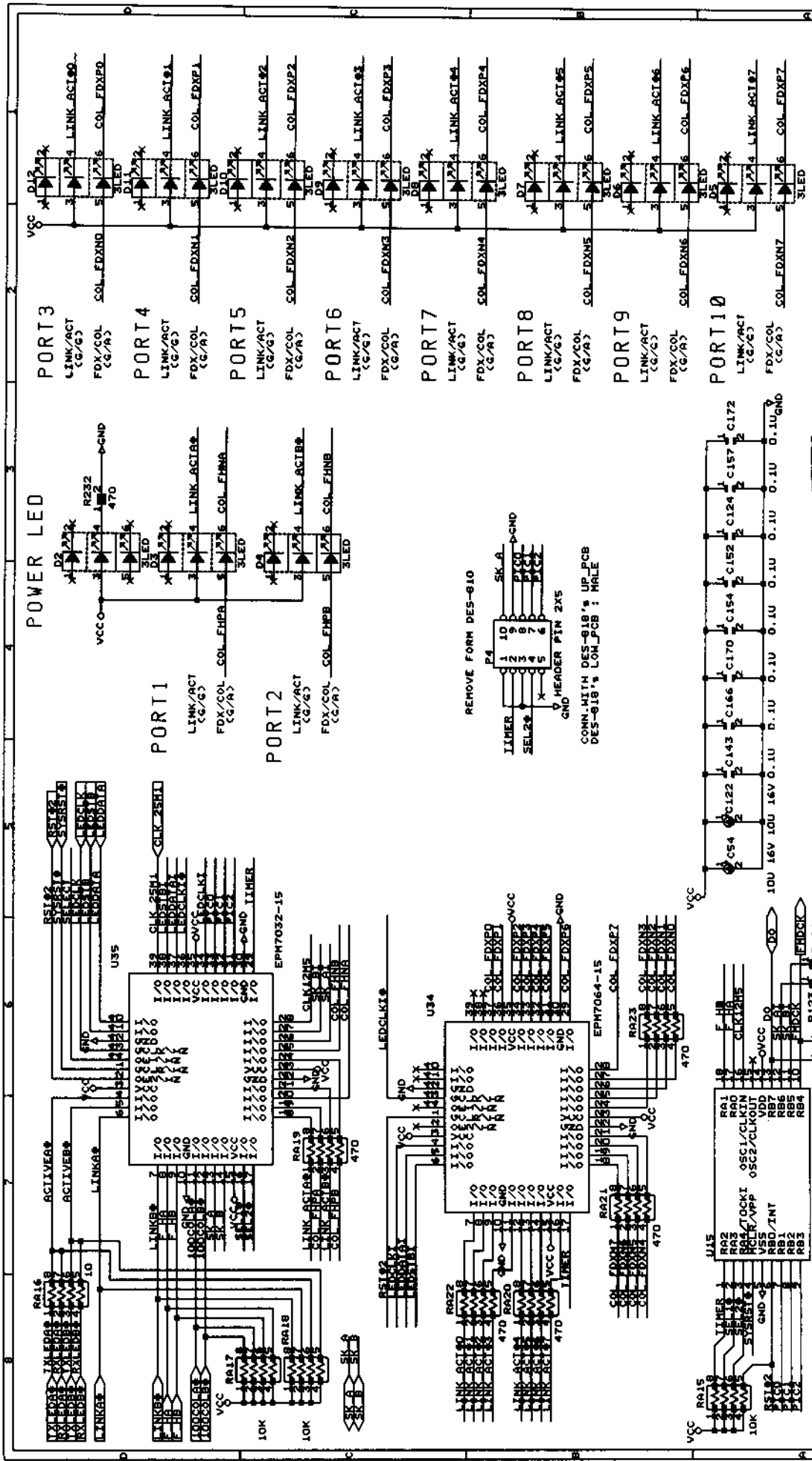
Size Document Number
REV A2

Date: February 15, 1998 Sheet 14 of 21



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Checkbook # 710	Issue Date :	
Prepared : POLLY PAN		

DES-BIO-UES-B18's LOW_FCB	REV
Size Document Number	
8	A2
Date: February 16, 1998	Sheet 16 of 21



D-Link DOC. NO.: 305DES18		Version: A Date: February 16, 1998	
Prepared: POLLY PAN Issued Date:		Size: 810/DES-818's LOW_PCB Size Document Number:	
Approved:		Date: February 16, 1998	
Title:		Date: February 16, 1998	
REV: A2		REV: A2	

REMOVE FROM DES-810

L2
BEAD CORE

VCC

C13
10U 16V
GND

P2

10 11 12 13 14 15 16 17 18 19 20

21 22 23 24 25 26 27 28 29 30 31

32 33 34 35 36 37 38 39 40 41 42

43 44 45 46 47 48 49 50 51 52 53 54

55 56 57 58 59 60 61 62 63 64 65 66

67 68 69 70 71 72 73 74 75 76 77 78

79 80 81 82 83 84 85 86 87 88 89 90

91 92 93 94 95 96 97 98 99 100 101 102

103 104 105 106 107 108 109 110 111 112 113 114

115 116 117 118 119 120 121 122 123 124 125 126

127 128 129 130 131 132 133 134 135 136 137 138

139 140 141 142 143 144 145 146 147 148 149 150

151 152 153 154 155 156 157 158 159 160 161 162

163 164 165 166 167 168 169 170 171 172 173 174

175 176 177 178 179 180 181 182 183 184 185 186

187 188 189 190 191 192 193 194 195 196 197 198

199 200 201 202 203 204 205 206 207 208 209 210

211 212 213 214 215 216 217 218 219 220 221 222

223 224 225 226 227 228 229 230 231 232 233 234

235 236 237 238 239 240 241 242 243 244 245 246

247 248 249 250 251 252 253 254 255 256 257 258

259 260 261 262 263 264 265 266 267 268 269 270

271 272 273 274 275 276 277 278 279 280 281 282

283 284 285 286 287 288 289 290 291 292 293 294

295 296 297 298 299 300 301 302 303 304 305 306

307 308 309 310 311 312 313 314 315 316 317 318

319 320 321 322 323 324 325 326 327 328 329 330

331 332 333 334 335 336 337 338 339 340 341 342

343 344 345 346 347 348 349 350 351 352 353 354

355 356 357 358 359 360 361 362 363 364 365 366

367 368 369 370 371 372 373 374 375 376 377 378

379 380 381 382 383 384 385 386 387 388 389 390

391 392 393 394 395 396 397 398 399 400 401 402

403 404 405 406 407 408 409 410 411 412 413 414

415 416 417 418 419 420 421 422 423 424 425 426

427 428 429 430 431 432 433 434 435 436 437 438

439 440 441 442 443 444 445 446 447 448 449 450

451 452 453 454 455 456 457 458 459 460 461 462

463 464 465 466 467 468 469 470 471 472 473 474

475 476 477 478 479 480 481 482 483 484 485 486

487 488 489 490 491 492 493 494 495 496 497 498

499 500 501 502 503 504 505 506 507 508 509 510

511 512 513 514 515 516 517 518 519 520 521 522

523 524 525 526 527 528 529 530 531 532 533 534

535 536 537 538 539 540 541 542 543 544 545 546

547 548 549 550 551 552 553 554 555 556 557 558

559 560 561 562 563 564 565 566 567 568 569 570

571 572 573 574 575 576 577 578 579 580 581 582

583 584 585 586 587 588 589 590 591 592 593 594

595 596 597 598 599 600 601 602 603 604 605 606

607 608 609 610 611 612 613 614 615 616 617 618

619 620 621 622 623 624 625 626 627 628 629 630

631 632 633 634 635 636 637 638 639 640 641 642

643 644 645 646 647 648 649 650 651 652 653 654

655 656 657 658 659 660 661 662 663 664 665 666

667 668 669 670 671 672 673 674 675 676 677 678

679 680 681 682 683 684 685 686 687 688 689 690

691 692 693 694 695 696 697 698 699 700 701 702

703 704 705 706 707 708 709 710 711 712 713 714

715 716 717 718 719 720 721 722 723 724 725 726

727 728 729 730 731 732 733 734 735 736 737 738

739 740 741 742 743 744 745 746 747 748 749 750

751 752 753 754 755 756 757 758 759 760 761 762

763 764 765 766 767 768 769 770 771 772 773 774

775 776 777 778 779 780 781 782 783 784 785 786

787 788 789 790 791 792 793 794 795 796 797 798

799 800 801 802 803 804 805 806 807 808 809 810

811 812 813 814 815 816 817 818 819 820 821 822

823 824 825 826 827 828 829 830 831 832 833 834

835 836 837 838 839 840 841 842 843 844 845 846

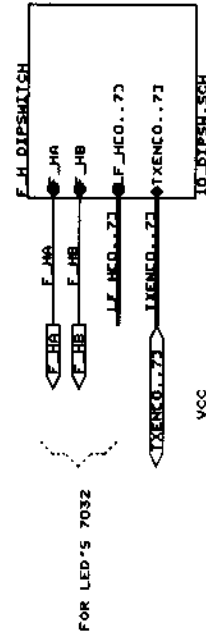
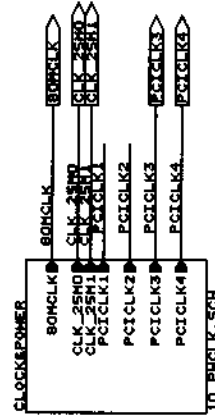
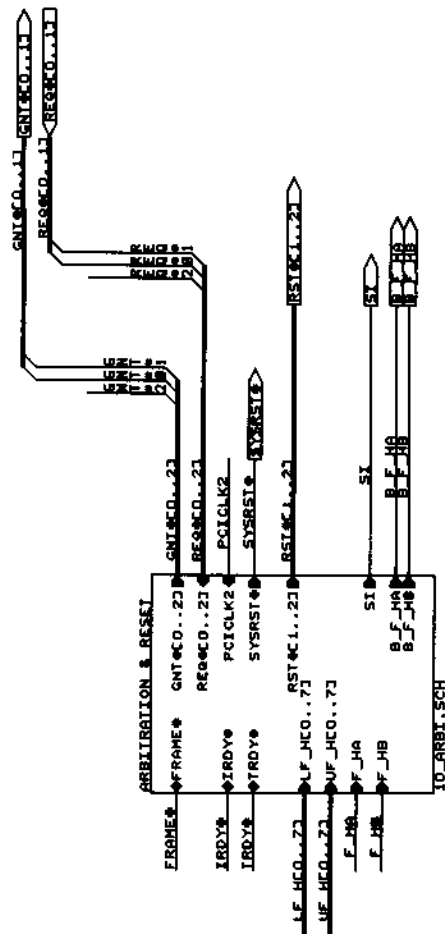
847 848 849 850 851 852 853 854 855 856 857 858

859 860 861 862 863 864 865 866 867 868 869 870

871 872 873 874 875 876 877 878 879 880 881 882

883 884 885 886 887 888 889 890 891 892 893 894

895 896 897 898 899 900 901 902 903 904 905 906</



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DOC. NO.: 305DES818-...A2-

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DES-010/DES-019, LOW PCB

REV A2 21

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OEM-0000-QQ-V10
bruary 16, 1986 Sheet

Version: A	Size: B	Date:
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Doc. No.: 305DES818

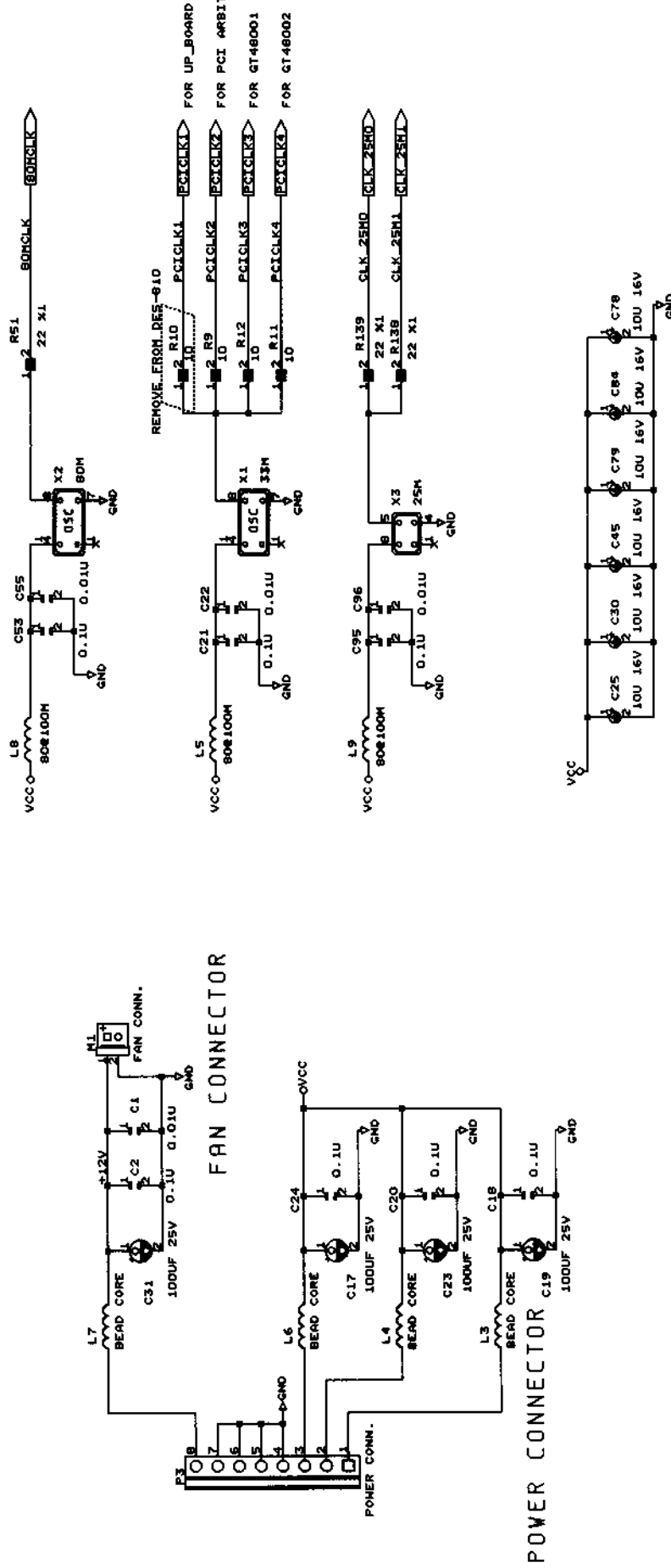
1000000

1000000

1000

i

POWER AND CLOCK GENERATOR



D-Link

DOC. No.: 305DES818---A2-

Approved: *[Signature]*
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 Version: A
 Title: DES-810/DES-818's LOW_PCB
 Size: Document Number
 Date: February 16, 1998
 Sheet: 19 of 21



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055-810/D55-818's LOW PCB

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Size document Number	REV
8	A2

~~2/33~~

